АС ЭНЕРГИЯ

Thyristor T373-2500-18

Mean on-state current				I_{TAV}		2500 A		
Repetitive peak off-state voltage				V_{DRM}		1000 - 1800 V		
Repetitive peak reverse voltage				V_{RRM}				
Turn-off time				t_q		250, 320, 400, 500 µs		
V_{DRM}, V_{RRM}, V	1000	1100	1200	1300	1400	1500	1600	1800
Voltage code	10	11	12	13	14	15	16	18
T_j, °C	-60 ÷ 125							

MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	Values	Test conditions	
ON-STATE					
I_{TAV}	Mean on-state current	A	2500 2806	T_c=90 °C, Double side cooled T_c=85 °C, Double side cooled 180° half-sine wave; 50 Hz	
I_{TRMS}	RMS on-state current	A	3925	T_c=90 °C, Double side cooled 180° half-sine wave; 50 Hz	
I_{TSM}	Surge on-state current	kA	69.0 79.0	T_j=$T_{j\,max}$ T_j=25 °C	180° half-sine wave; t_p=10 ms; single pulse; V_D=V_R=0 V; Gate pulse: I_G=2 A; t_{GP}=50 ms; di_G/dt≥1 A/ms
			72.0 83.0	T_j=$T_{j\,max}$ T_j=25 °C	180° half-sine wave; t_p=8.3 ms; single pulse; V_D=V_R=0 V; Gate pulse: I_G=2 A; t_{GP}=50 ms; di_G/dt≥1 A/ms
I^2t	Safety factor	$A^2s\cdot10^3$	23800 31200	T_j=$T_{j\,max}$ T_j=25 °C	180° half-sine wave; t_p=10 ms; single pulse; V_D=V_R=0 V; Gate pulse: I_G=2 A; t_{GP}=50 ms; di_G/dt≥1 A/ms
			21500 28500	T_j=$T_{j\,max}$ T_j=25 °C	180° half-sine wave; t_p=8.3 ms; single pulse; V_D=V_R=0 V; Gate pulse: I_G=2 A; t_{GP}=50 ms; di_G/dt≥1 A/ms
BLOCKING					
V_{DRM}, V_{RRM}	Repetitive peak off-state and Repetitive peak reverse voltages	V	1000 - 1800	$T_{j\,min}$< T_j <$T_{j\,max}$; 180° half-sine wave; 50 Hz; Gate open	
V_{DSM}, V_{RSM}	Non-repetitive peak off-state and Non-repetitive peak reverse voltages	V	1100 - 1900	$T_{j\,min}$< T_j <$T_{j\,max}$; 180° half-sine wave; single pulse; Gate open	
V_D, V_R	Direct off-state and Direct reverse voltages	V	$0.6\cdot V_{DRM}$ $0.6\cdot V_{RRM}$	T_j=$T_{j\,max}$; Gate open	

TRIGGERING				
I_{FGM}	Peak forward gate current	A	10	$T_j=T_{j\,max}$
V_{RGM}	Peak reverse gate voltage	V	5	
P_G	Gate power dissipation	W	5	$T_j=T_{j\,max}$ for DC gate current
SWITCHING				
$(di_T/dt)_{crit}$	Critical rate of rise of on-state current non-repetitive (f=1 Hz)	A/ms	2000	$T_j=T_{j\,max}$; $V_D=0.67\cdot V_{DRM}$; I_{TM}=6400 A; Gate pulse: I_G=2 A; t_{GP}=50 ms; $di_G/dt\geq$2 A/ms
THERMAL				
T_{stg}	Storage temperature	°C	-60...50	
T_j	Operating junction temperature	°C	-60...125	
MECHANICAL				
F	Mounting force	kN	40.0...50.0	
a	Acceleration	m/s^2	50	Device clamped

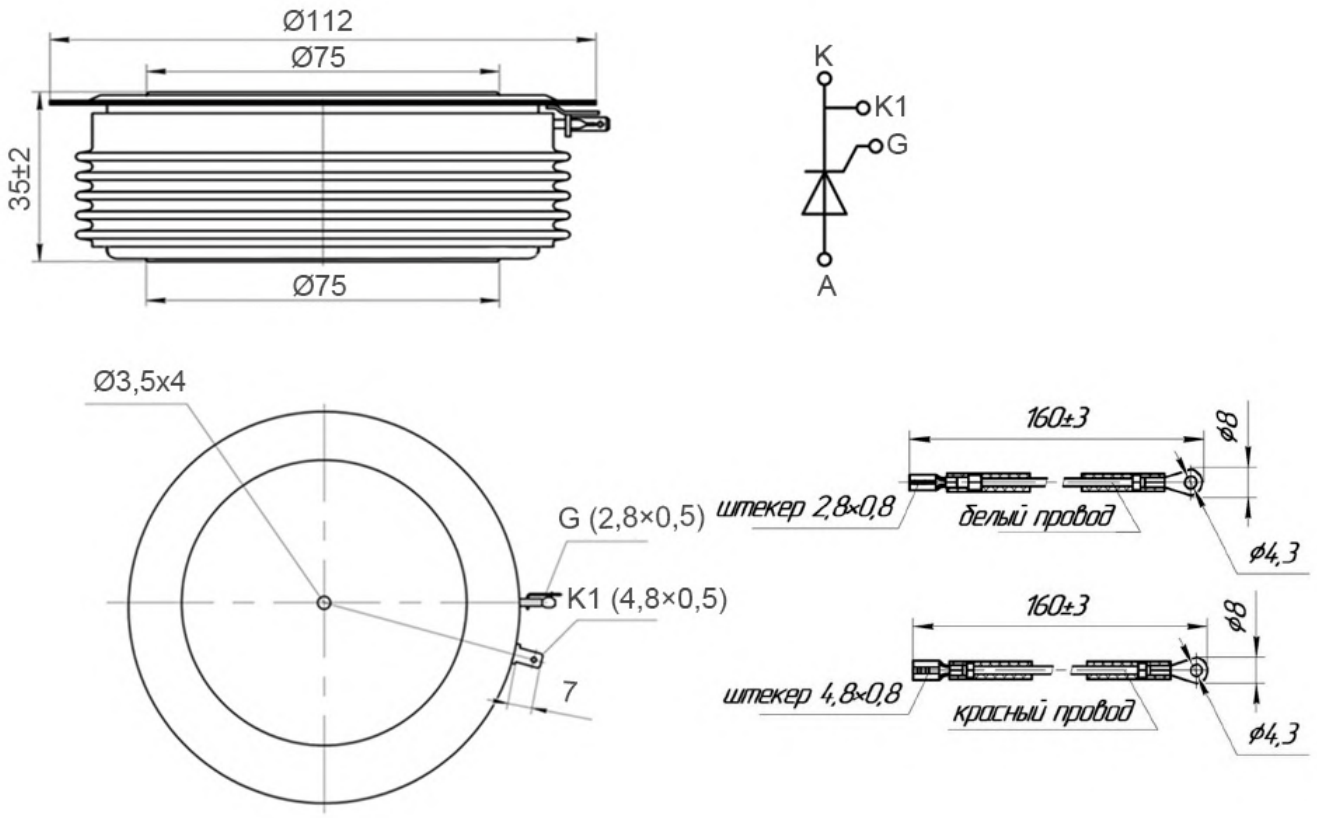
CHARACTERISTICS

Symbols and parameters		Units	Values	Conditions	
ON-STATE					
V_{TM}	Peak on-state voltage, max	V	1.55	T_j=25 °C; I_{TM}=7850 A	
$V_{T(TO)}$	On-state threshold voltage, max	V	0.909	$T_j=T_{j\,max}$; 0.5 p I_{TAV} < I_T < 1.5 p I_{TAV}	
r_T	On-state slope resistance, max	mW	0.075		
I_L	Latching current, max	mA	1500	T_j=25 °C; V_D=12 V; Gate pulse: I_G=2 A; t_{GP}=50 ms; $di_G/dt\geq$1 A/ms	
I_H	Holding current, max	mA	300	T_j=25 °C; V_D=12 V; Gate open	
BLOCKING					
I_{DRM}, I_{RRM}	Repetitive peak off-state and Repetitive peak reverse currents, max	mA	300	$T_j=T_{j\,max}$; $V_D=V_{DRM}$; $V_R=V_{RRM}$	
$(dv_D/dt)_{crit}$	Critical rate of rise of off-state voltage[1)], min	V/ms	200, 320, 500, 1000, 1600, 2000, 2500	$T_j=T_{j\,max}$; $V_D=0.67\cdot V_{DRM}$; Gate open	
TRIGGERING					
V_{GT}	Gate trigger direct voltage, max	V	3.00 2.50 1.50	T_j= $T_{j\,min}$ T_j=25 °C T_j= $T_{j\,max}$	V_D=12 V; I_D=3 A; Direct gate current
I_{GT}	Gate trigger direct current, max	mA	400 250 150	T_j= $T_{j\,min}$ T_j= 25 °C T_j= $T_{j\,max}$	
V_{GD}	Gate non-trigger direct voltage, min	V	0.35	$T_j=T_{j\,max}$; $V_D=0.67\cdot V_{DRM}$; Direct gate current	
I_{GD}	Gate non-trigger direct current, min	mA	30.00		
SWITCHING					
t_{gd}	Delay time, max	ms	0.90	T_j=25 °C; V_D=1000 V; $I_{TM}=I_{TAV}$; di/dt=200 A/ms; Gate pulse: I_G=2 A; V_G=20 V; t_{GP}=50 ms; di_G/dt=2 A/ms	
t_{gt}	Turn-on time, max	ms	3.00		
t_q	Turn-off time[2)], max	ms	250, 320, 400, 500	dv_D/dt=50 V/ms; $T_j=T_{j\,max}$; $I_{TM}=I_{TAV}$; di_R/dt=-10 A/ms; V_R=100V; $V_D=0.67\cdot V_{DRM}$	
Q_{rr}	Total recovered charge, max	mC	2970	$T_j=T_{j\,max}$; I_{TM}= 1600 A; di_R/dt=-10 A/ms; V_R=100 V	
t_{rr}	Reverse recovery time, max	ms	29		
I_{rrM}	Peak reverse recovery current, max	A	205		

THERMAL					
R_{thjc}	Thermal resistance, junction to case, max	°C/W	0.010	Direct current	Double side cooled
R_{thjc-A}			0.022		Anode side cooled
R_{thjc-K}			0.018		Cathode side cooled
R_{thck}	Thermal resistance, case to heatsink, max	°C/W	0.002	Direct current	
MECHANICAL					
w	Weight, max	g	1700		
D_s	Surface creepage distance	mm (inch)	55.13 (2.170)		
D_a	Air strike distance	mm (inch)	25.10 (0.988)		

OVERALL DIMENSIONS

Package type: PT74



K – cathode;

A – anode;

K1 – auxiliary cathode;

G – gate;

All dimensions in millimeters

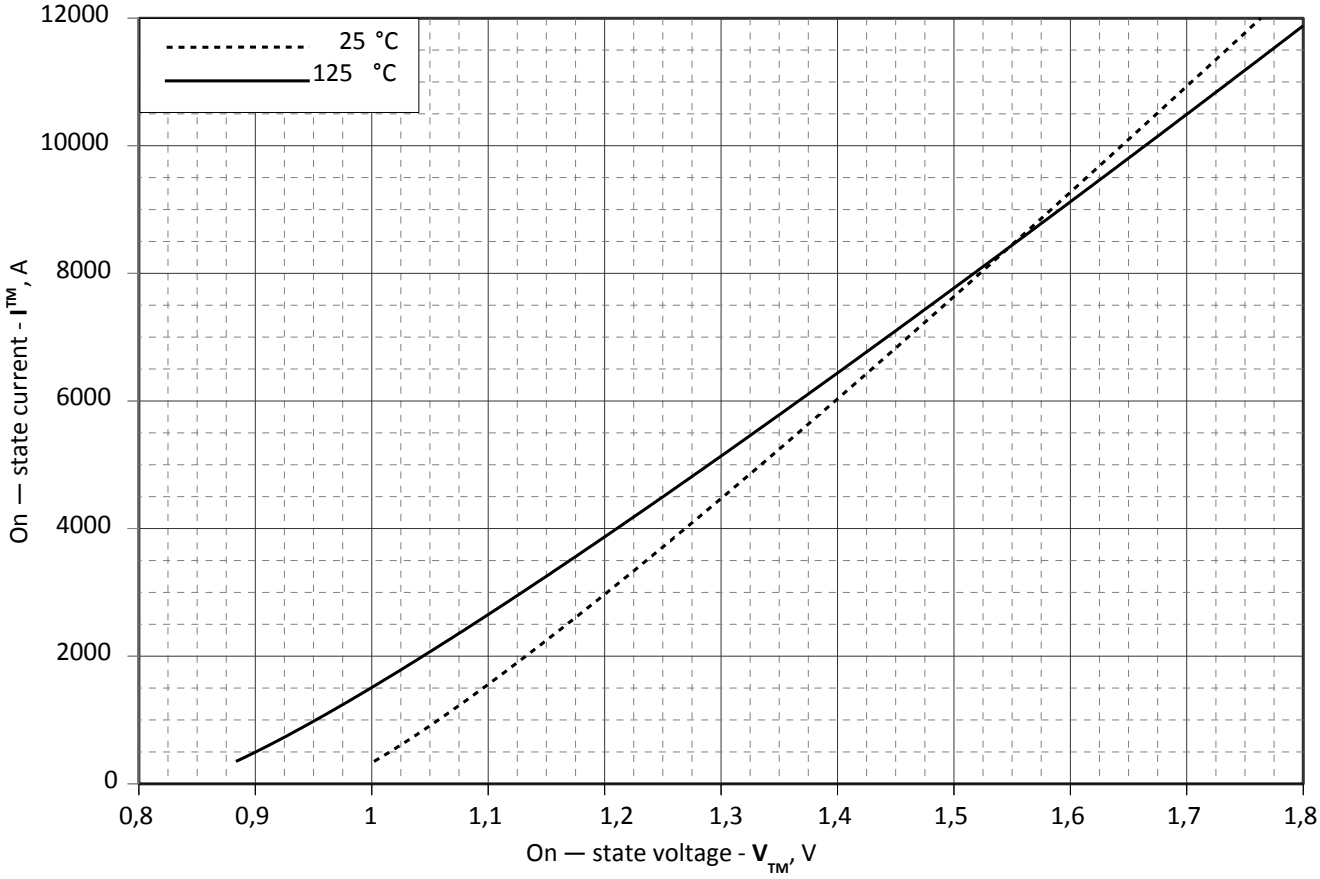


Fig 1 – On-state characteristics of Limit device

Analytical function for On-state characteristic:

$$V_T = A + B \cdot i_T + C \cdot \ln(i_T + 1) + D \cdot \sqrt{i_T}$$

	Coefficients for max curves	
	$T_j = 25^oC$	$T_j = T_{j\,max}$
A	0.976510000	0.844860000
B	0.000049556	0.000059368
C	-0.005807600	-0.005890600
D	0.002254800	0.002798800

On-state characteristic model (see Fig. 1)

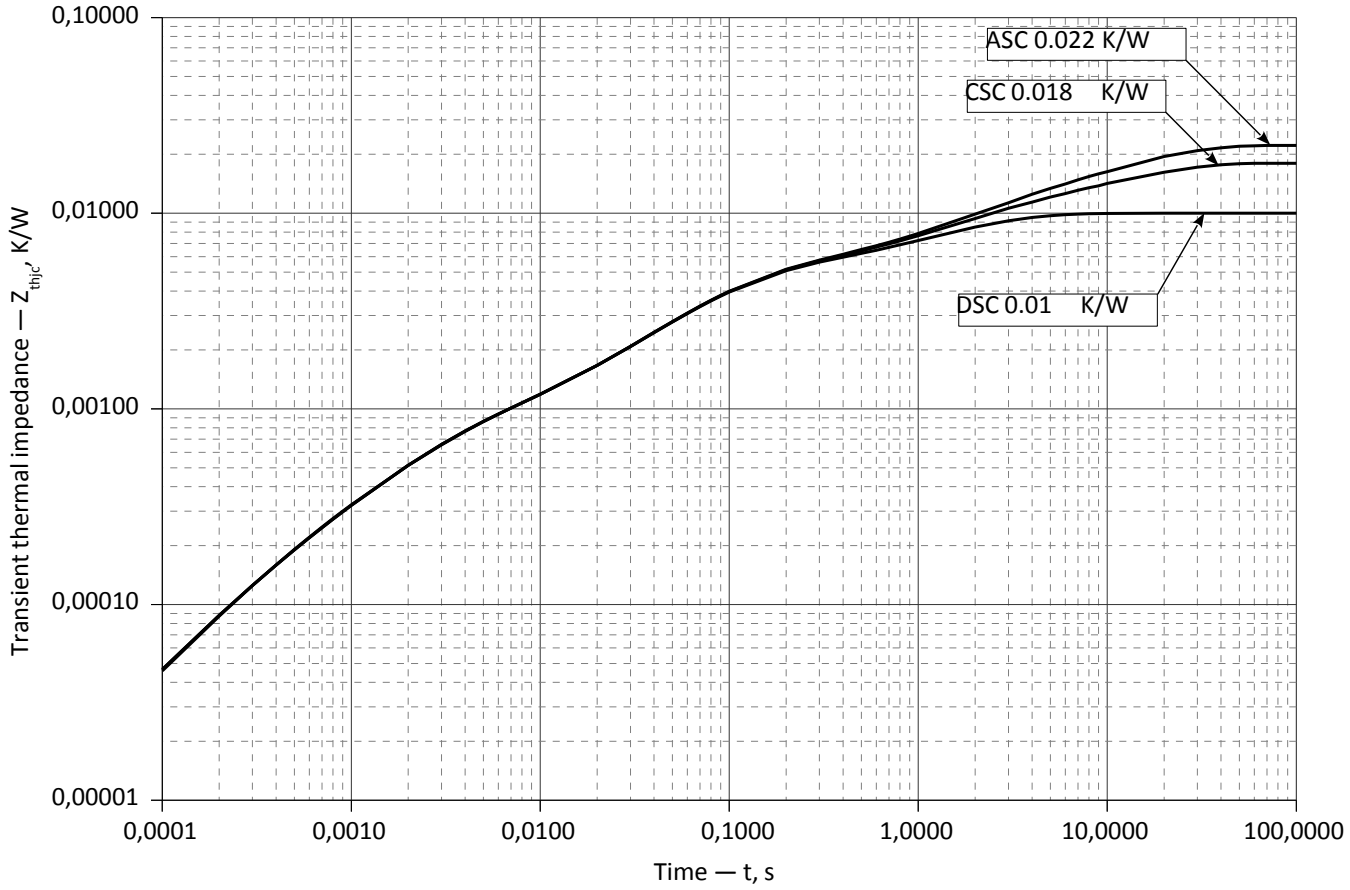


Fig 2 – Transient thermal impedance Z_{thjc} vs. time t

Analytical function for Transient thermal impedance junction to case Z_{thjc} for DC:

$$Z_{thjc} = \sum_{i=1}^{n} R_i \left(1 - e^{-\frac{t}{\tau_i}} \right)$$

Where ***i*** = 1 to ***n***, ***n*** is the number of terms in the series.

t = Duration of heating pulse in seconds. **Z_{thjc}**

= Thermal resistance at time t.

R_i = Amplitude of p_{th} term.

t_i = Time constatnt of r_{th} term.

DC Double side cooled

i	1	2	3	4	5	6
R_i, K/W	0.002785	0.003537	0.0005787	0.0006418	0.00009446	0.002362
t_i, s	2.061	0.07354	0.002615	0.1375	0.0004601	1.210

DC Anode side cooled

i	1	2	3	4	5	6
R_i, K/W	0.01246	0.00478	0.0006333	0.003716	0.0005969	0.00006119
t_i, s	13.31	1.871	0.2261	0.07337	0.002363	0.0003248

DC Cathode side cooled

i	1	2	3	4	5	6
R_i, K/W	0.008256	0.004771	0.0006239	0.003744	0.0005969	0.00006164
t_i, s	13.25	1.783	0.2371	0.07347	0.002367	0.000327

Transient thermal impedance junction to case Z_{thjc} model (see Fig. 2)

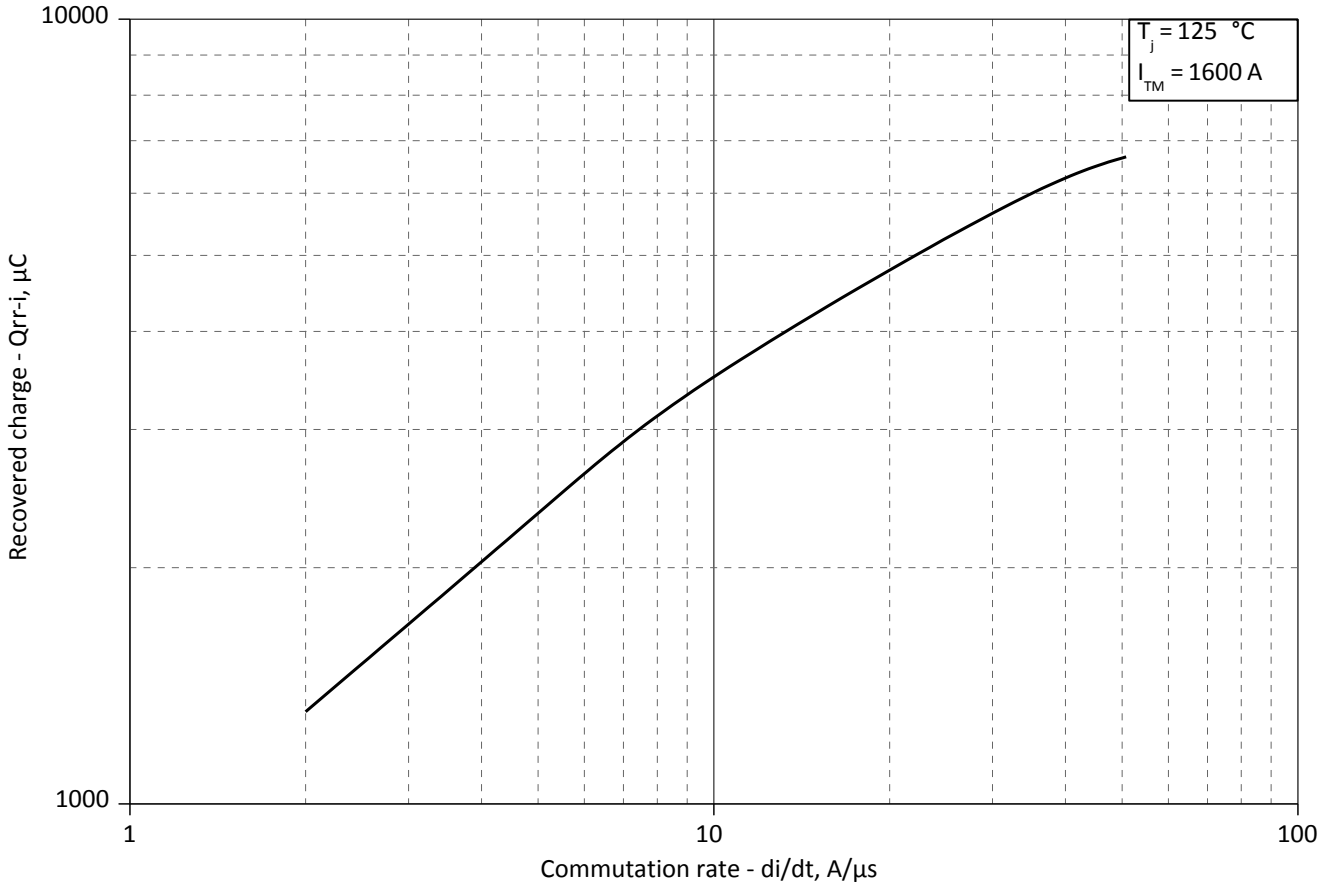


Fig 3 – Maximum recovered charge Q_{rr-i} (integral) vs. commutation rate di_R/dt

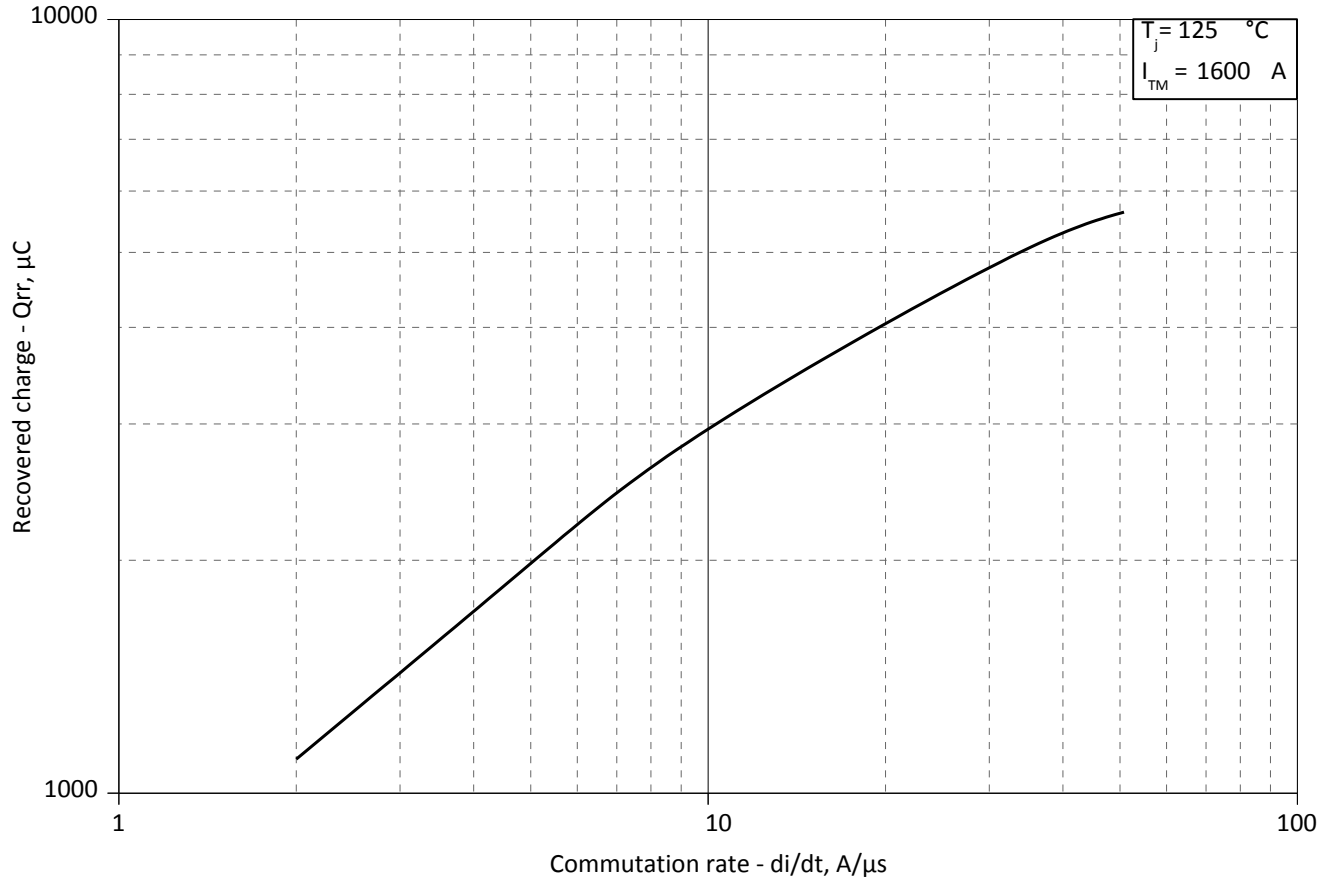


Fig 4 – Maximum recovered charge Q_{rr} vs. commutation rate di_R/dt (25% chord)

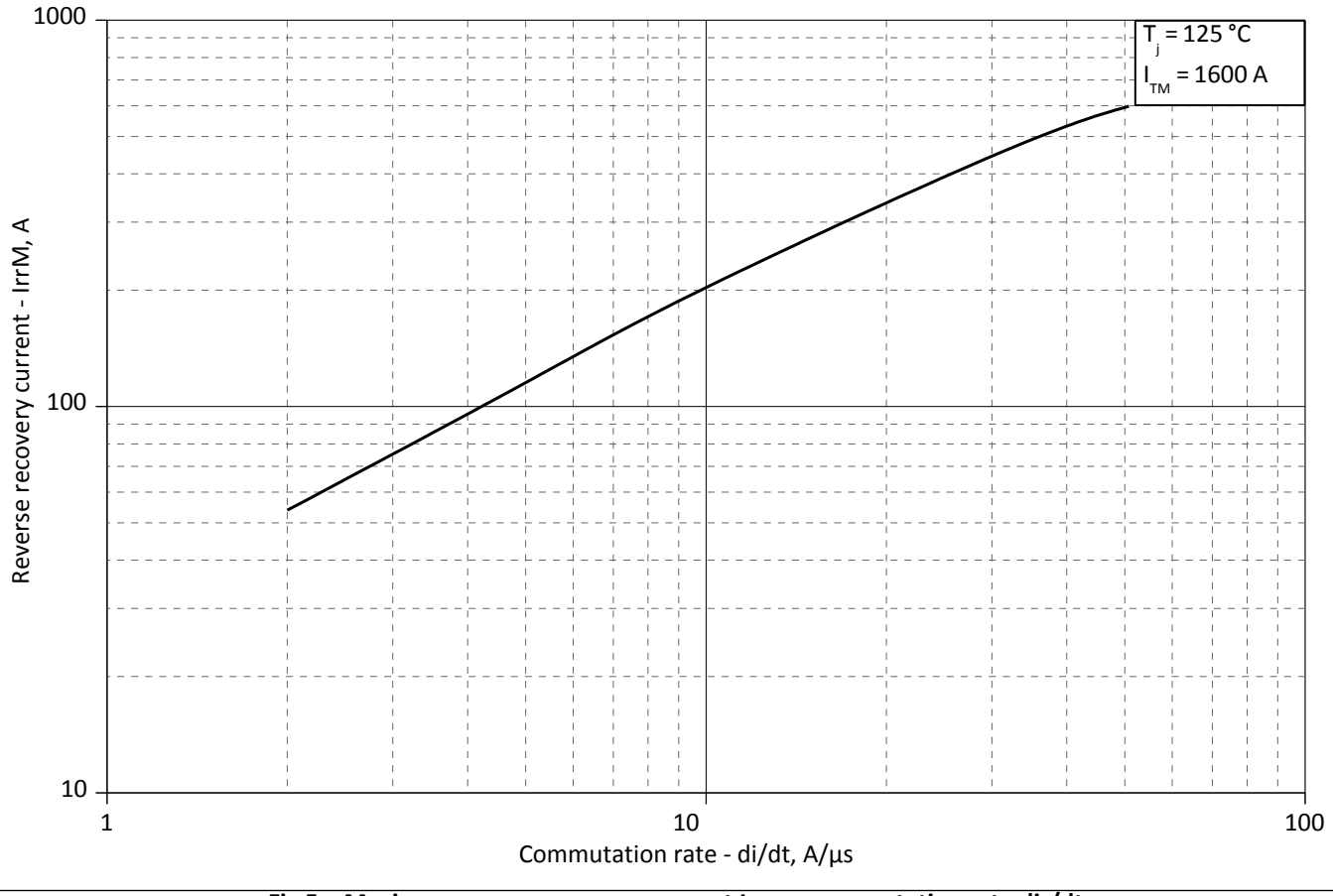


Fig 5 – Maximum reverse recovery current I_{rrM} vs. commutation rate di_R/dt

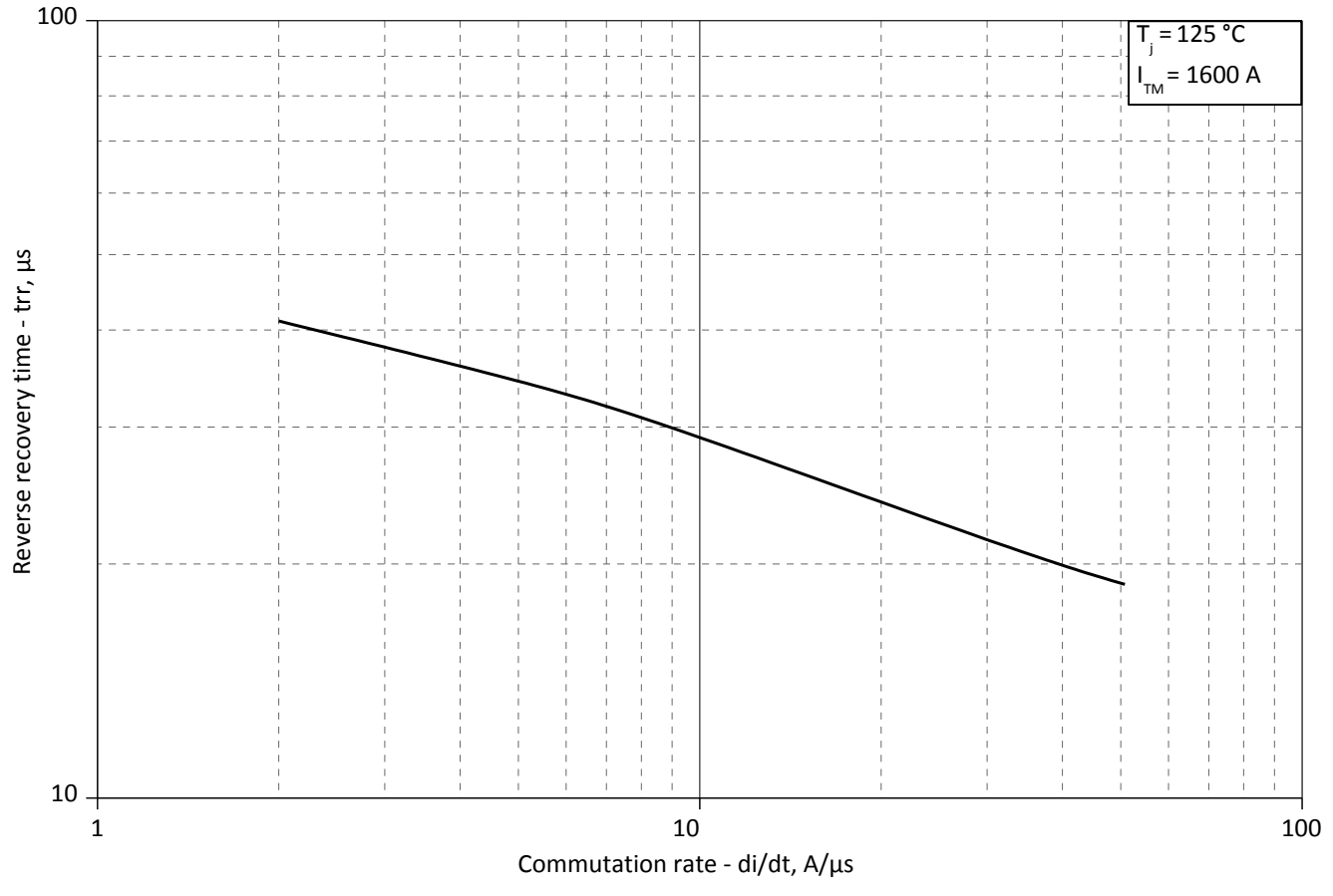


Fig 6 – Maximum recovery time t_{rr} vs. commutation rate di_R/dt (25% chord)

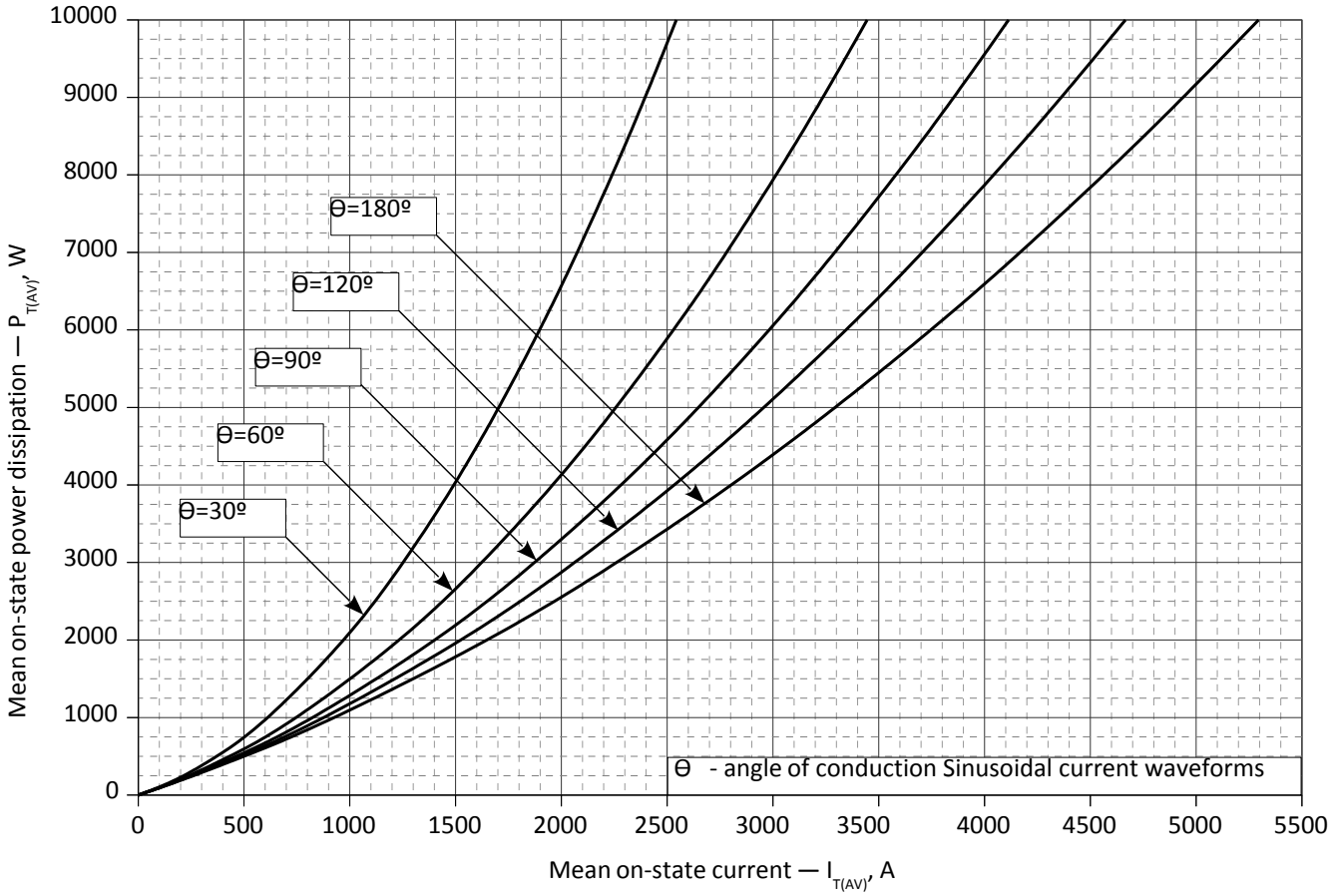


Fig. 7 - Mean on-state power dissipation P_{TAV} vs. mean on-state current I_{TAV} for sinusoidal current waveforms at different conduction angles (f=50Hz, DSC)

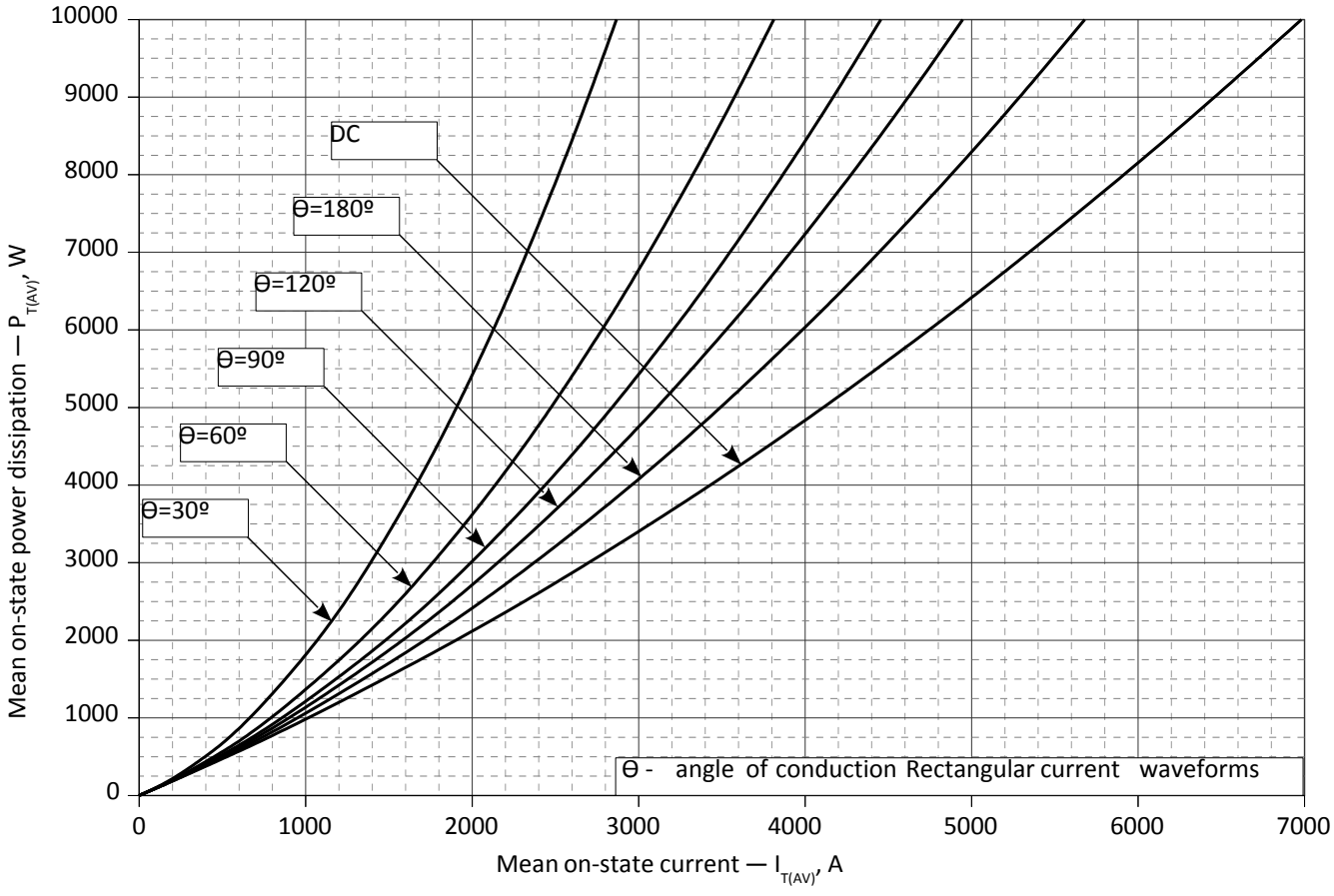


Fig. 8 – Mean on-state power dissipation P_{TAV} vs. mean on-state current I_{TAV} for rectangular current waveforms at different conduction angles and for DC (f=50Hz, DSC)

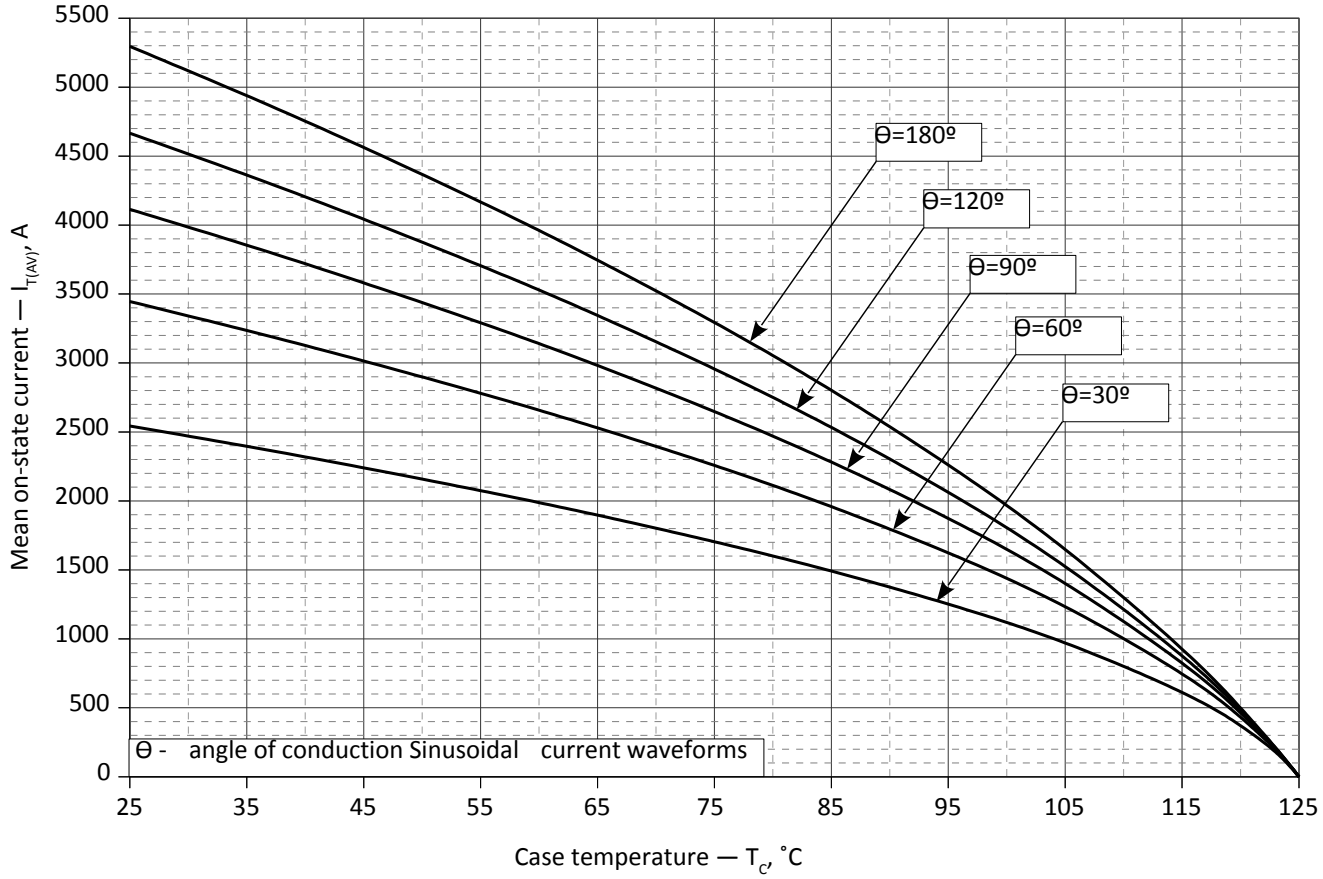


Fig. 9 – Mean on-state current I_{TAV} vs. case temperature T_C for sinusoidal current waveforms at different conduction angles (f=50Hz, DSC)

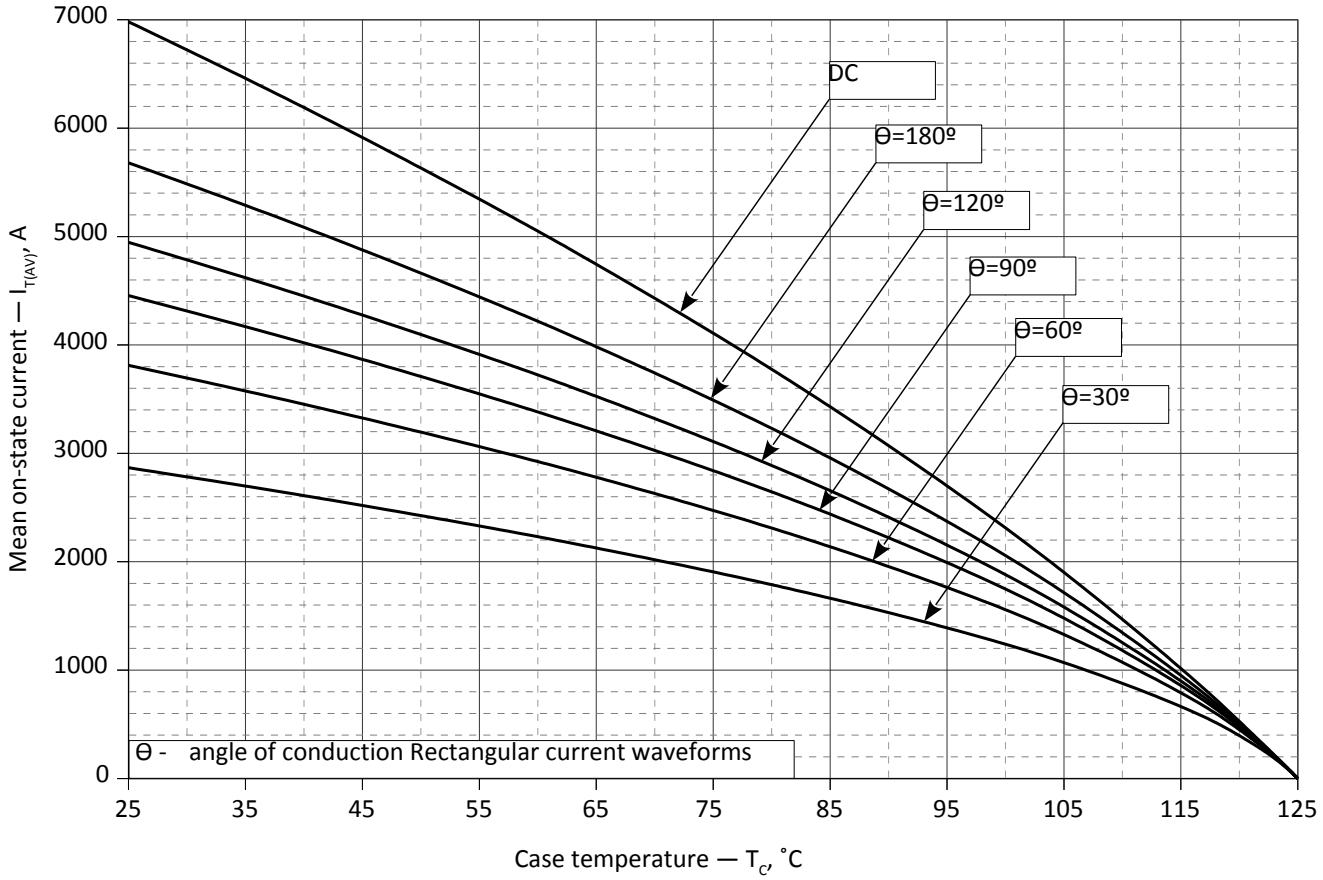


Fig. 10 - Mean on-state current I_{TAV} vs. case temperature T_C for rectangular current waveforms at different conduction angles and for DC (f=50Hz, DSC)

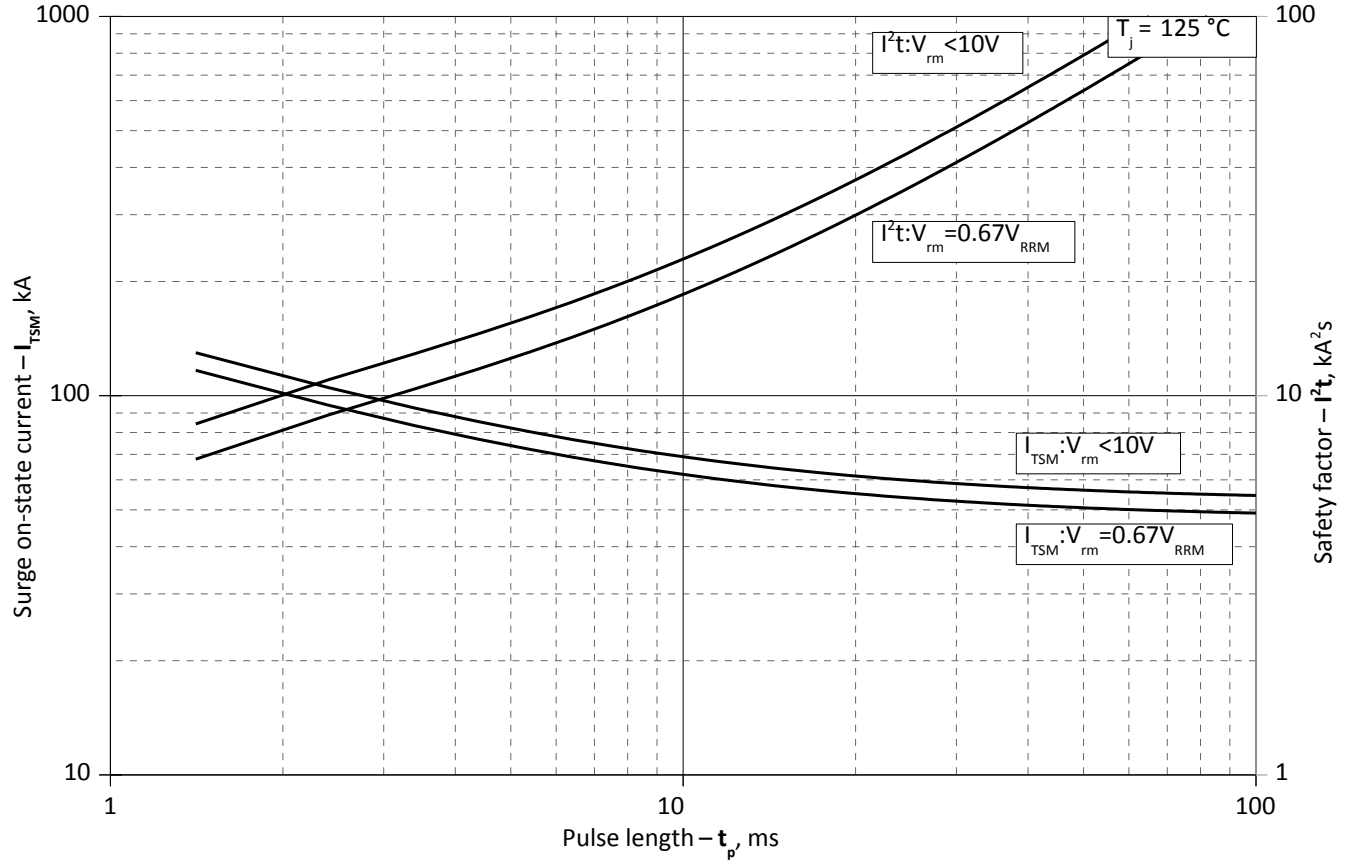


Fig. 11 – Maximum surge on-state current I_{TSM} and safety factor I^2t vs. pulse length t_p

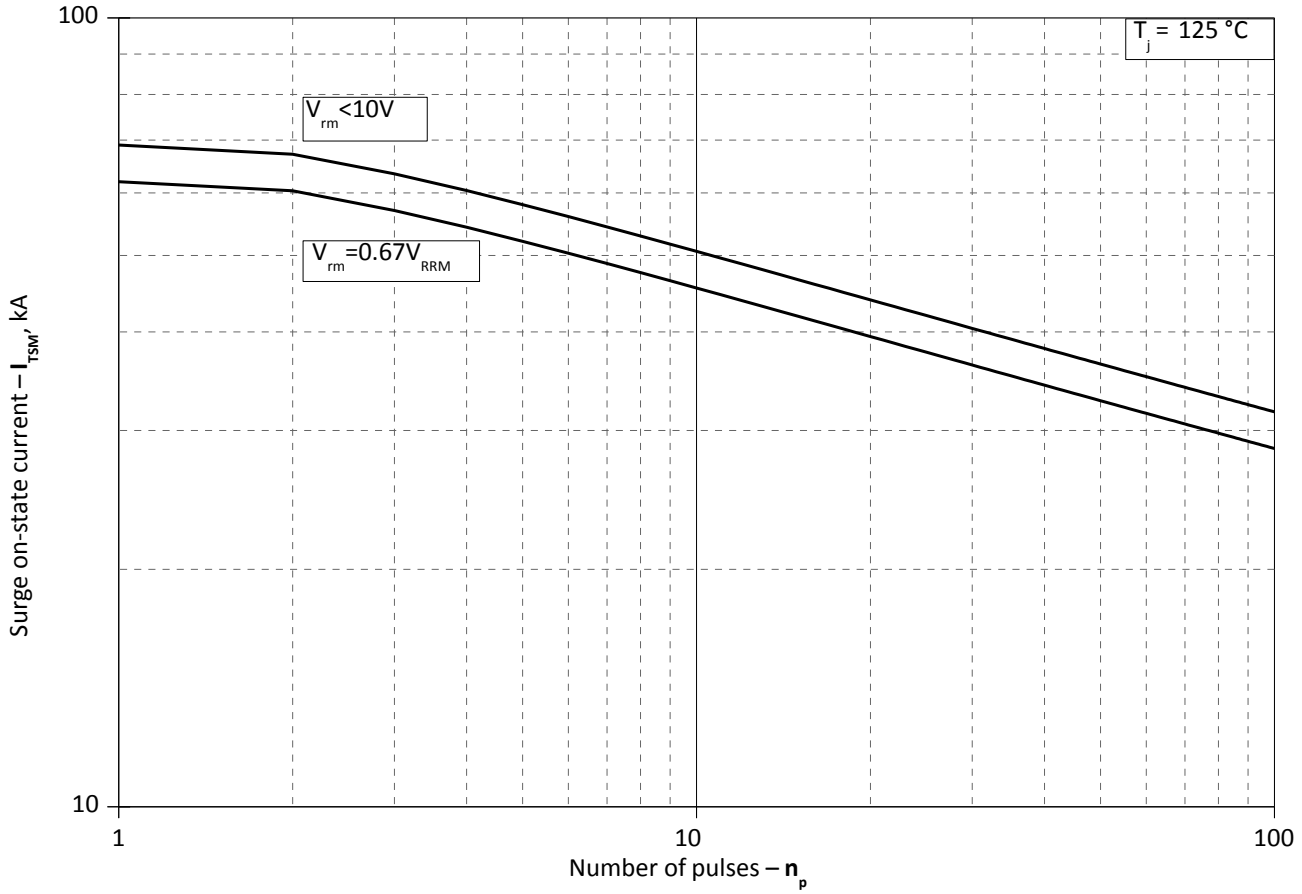


Fig. 12 - Maximum surge on-state current I_{TSM} vs. number of pulses n_p