

Gate Commutated Turn-Off Thyristors AFGC 1500-130-A-DS



Key Parameters

I_{TQRM}	=	1500 A
$I_{T(AV)}$	=	500 A
V_{DRM}	=	6500 V

Application

- Inverters
- D.C. choppers
- Induction heaters
- D.C. to D.C. converters

MAXIMUM RATINGS

Symbols and parameters			Voltage class	Unit
V_{RRM}	Repetitive peak reverse voltage		6500	V
V_{RSM}	Non-repetitive peak reverse voltage		6500	V
V_{DRM}	DC reverse voltage	$V_{GK} = -2V$	6500	V
V_{DSM}	Repetitive peak off-state voltage	$V_{GK} = -2V$	6500	V
V_{LTDS}	Non-repetitive peak off-state voltage	$V_{GK} = -2V, \lambda = 100 \text{ Fit}$	3600	V

Symbols and parameters			Value	Unit
I_{TQRM}	Repetitive controllable on-state current	$V_{DM} = 3/4 V_{DRM}, V_D = 3000V, L_C = 0.3\mu H, V_{RG} = 20V, T_j = 25/115^\circ C$	1500	A
$I_{T(RMS)}$	RMS on-state current	Applied for all conduction angles $f = 60\text{Hz}, \text{sinewave}$ $\theta = 180^\circ, T_f = 55^\circ C$	780	A
$I_{T(AV)}$	Average on-state current		500	A
I_{TSM}	Surge (non-repetitive) on-state current	One half cycle at 60Hz, $T_j = 115^\circ C \text{ Start}$	8	kA
I^2t	Current-squared, time integration		2.7×10^5	A^2s
di_T/dt	Critical rate of rise of on-state current	$V_D = 3000V, I_T = 1500A, C_S = 0.2\mu F, R_S = 5\Omega, T_j = 25/115^\circ C, f = 60\text{Hz}$	1000	$A/\mu s$
V_{FGM}	Peak forward gate voltage		10	V
V_{RGM}	Peak reverse gate voltage		21	V

Symbols and parameters			Value	Unit
I_{FGM}	Peak forward gate current		900	A
I_{RGM}	Max. RMS on-state current		1500	A
P_{FGM}	Peak forward gate power dissipation		9	kW
P_{RGM}	Peak reverse gate power dissipation		32	kW
$P_{FG(AV)}$	Max. peak non-repetitive surge current		180	W
$P_{RG(AV)}$	Limiting load integral		230	W

ELECTRICAL CHARACTERISTICS

Symbols and parameters			Value			Unit
			min	typ	max	
V_{TM}	On-state voltage	$I_T = 1500A, T_j = 115^\circ C$			7.0	V
I_{RRM}	Repetitive peak reverse current	$V_{RM} = 6500V, T_j = 115^\circ C$			220	mA
I_{DRM}	Repetitive peak off-state current	$V_{DM} = 6500V, V_{GK} = -2V, T_j = 115^\circ C$			150	mA
I_{GRM}	Reverse gate current	$V_{RG} = 21V, T_j = 115^\circ C$			100	mA
dv/dt	Critical rate of rise of off-state voltage	$V_D = 3000V, V_{GK} = -2V, T_j = 115^\circ C$ (Expo. wave)	3000			V/ μs
t_{gt}	Turn-on time	$I_T = 1500A, V_D = 3000V, di/dt = 1000A/\mu s, T_j = 115^\circ C, C_S = 0.2\mu F, R_S = 5\Omega$			5	μs
t_d	Delay time				1	μs
E_{on}	Peak gate turn-off current				2.15	J/P
t_s	Gate trigger voltage	$I_T = 1500A, V_D = 3000V, V_{DM} = 3/4 V_{DRM}, C_S = 0.2\mu F, R_S = 5\Omega, V_{RG} = 20V, T_j = 115^\circ C$			3	μs
E_{off}	Gate trigger current				12	J/P
Q_{RR}	Delay time	$V_R = 3000V, I_T = 1500A, di/dt = 1000A/\mu s, C_S = 0.2\mu F, R_S = 5\Omega, T_j = 115^\circ C$			2800	μC
E_{rec}	Peak gate turn-off current				7	A
I_{GT}	Gate trigger voltage	DC METHOD : $V_D = 24V, R_L = 0.1\Omega, T_j = 25^\circ C$			0.75	A
V_{GT}	Gate trigger current				1.5	V

THERMAL

Symbols and parameters			Value	Unit
T_j	Junction operating temperature		-20 ... 115	°C
T_{stg}	Storage temperature range		-20 ... 150	°C
$R_{th(j-f)}$	Thermal resistance, max	Junction to fin	0.016	°C/W

MECHANICAL

Symbols and parameters			Value	Unit
M	Mounting force required	Recommended value 20	18 ... 24	kN
w	Weight		760	g

DIMENSIONES

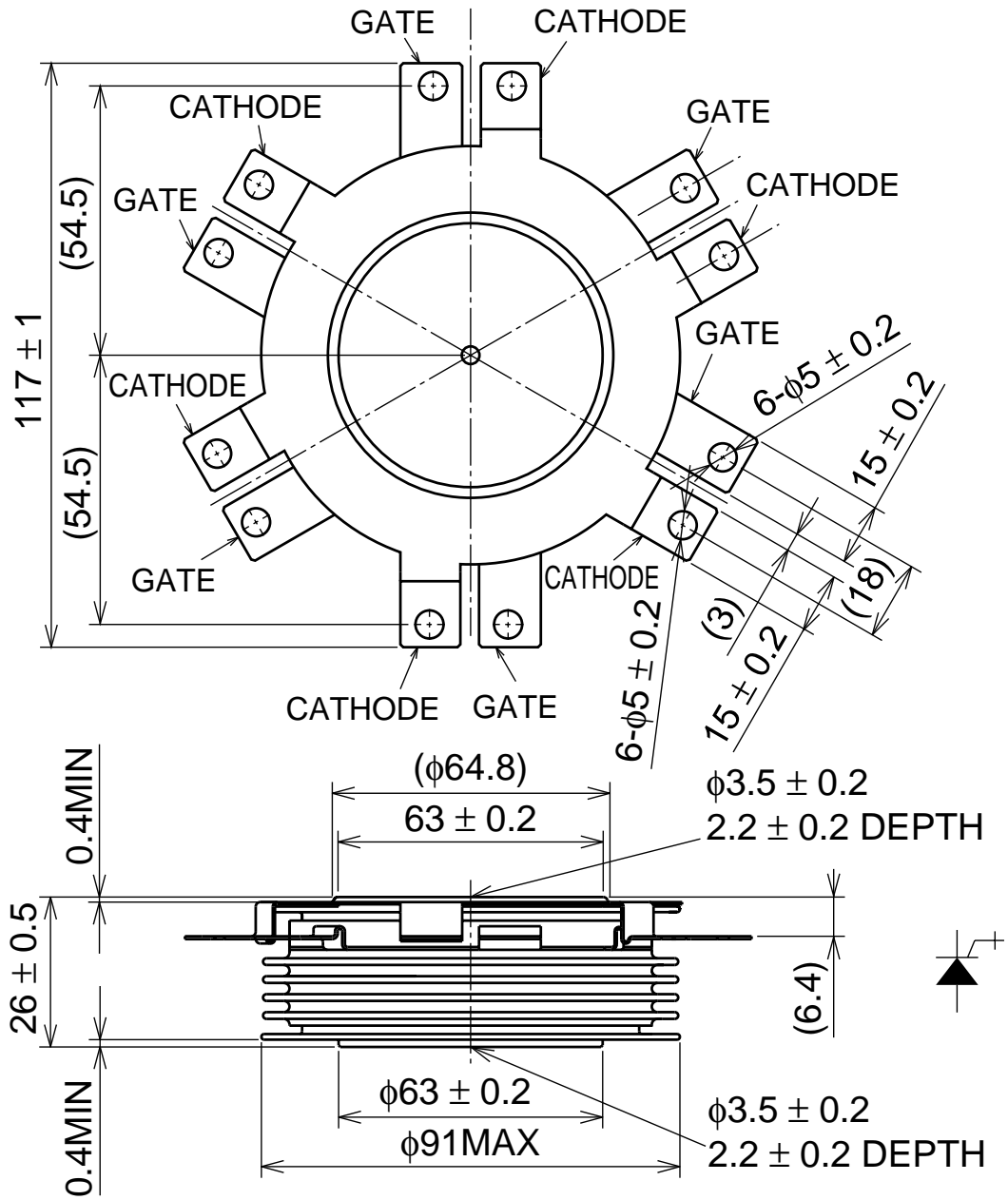


Fig. 1 Turn-on and Turn-off waveform

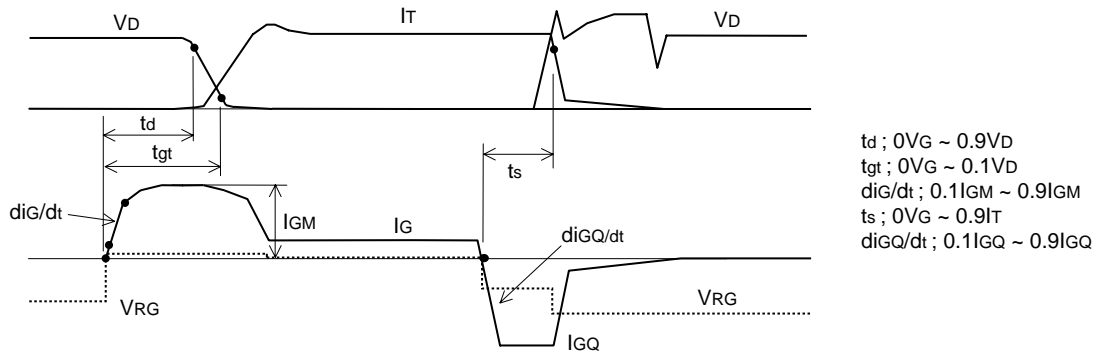


Fig. 2 Turn-on test circuit

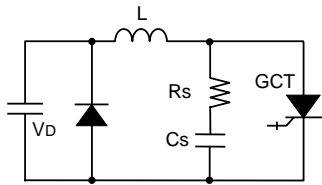


Fig. 3 Turn-off test circuit (With clamp circuit)

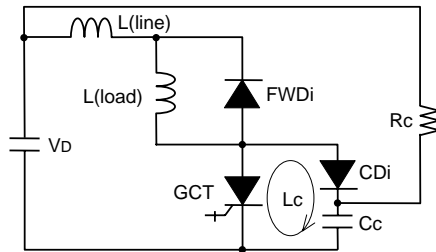


Fig. 4 dv/dt test waveform

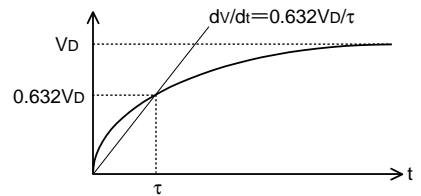
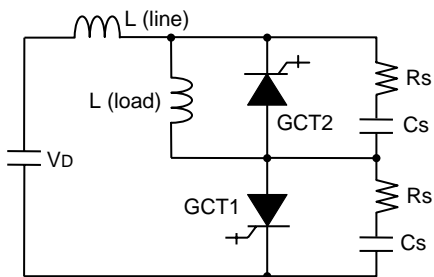
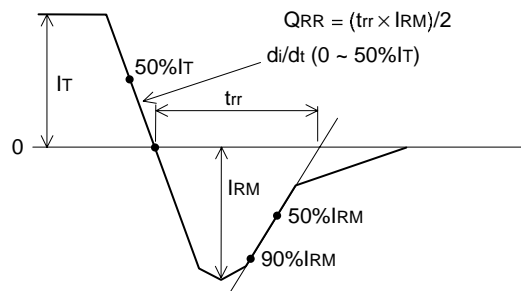


Fig. 5 Turn-off and Recovery test circuit

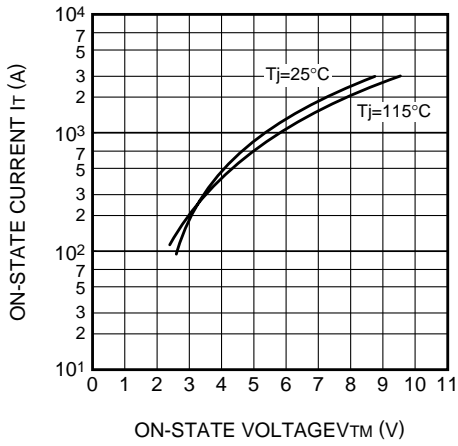


GCT1 : For turn-off test
GCT2 : For Recovery test

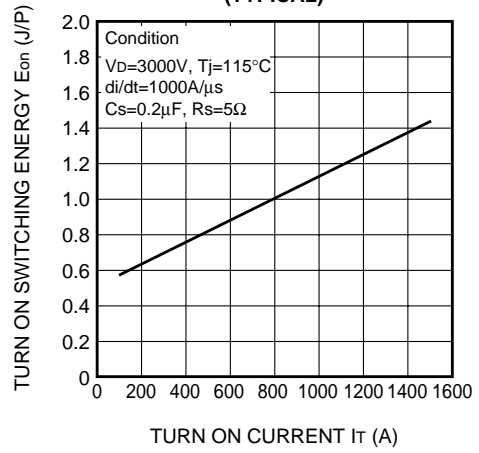
Fig. 6 Reverse recovery waveform



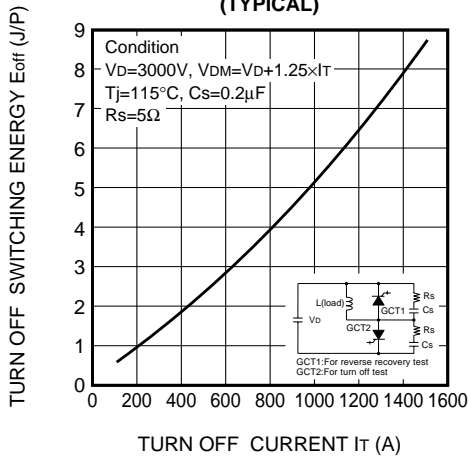
MAXIMUM ON-STATE CHARACTERISTIC



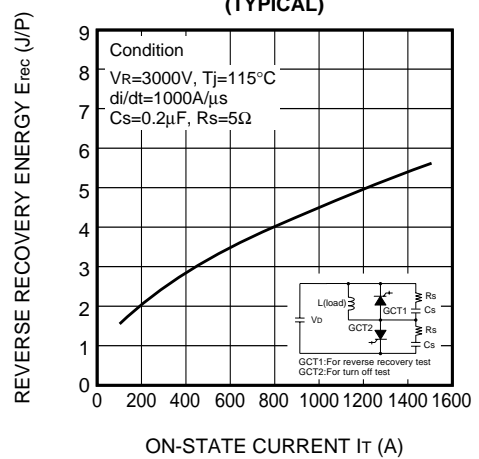
E_{on} VS I_T (TYPICAL)



E_{off} VS I_T (TYPICAL)



E_{rec} VS I_T (TYPICAL)



MAXIMUM THERMAL IMPEDANCE CHARACTERISTIC (JUNCTION TO FIN)

