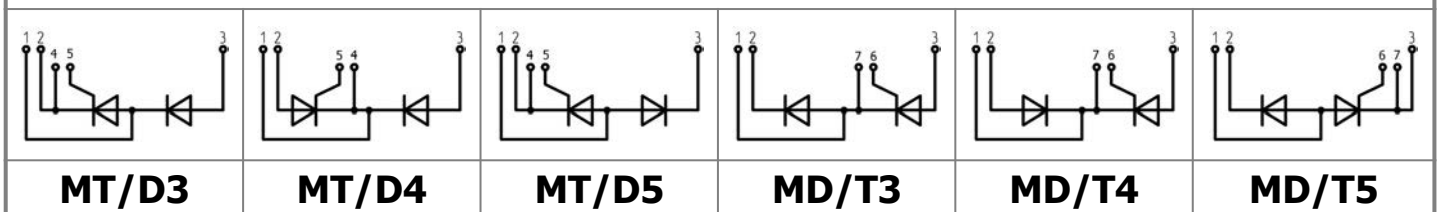
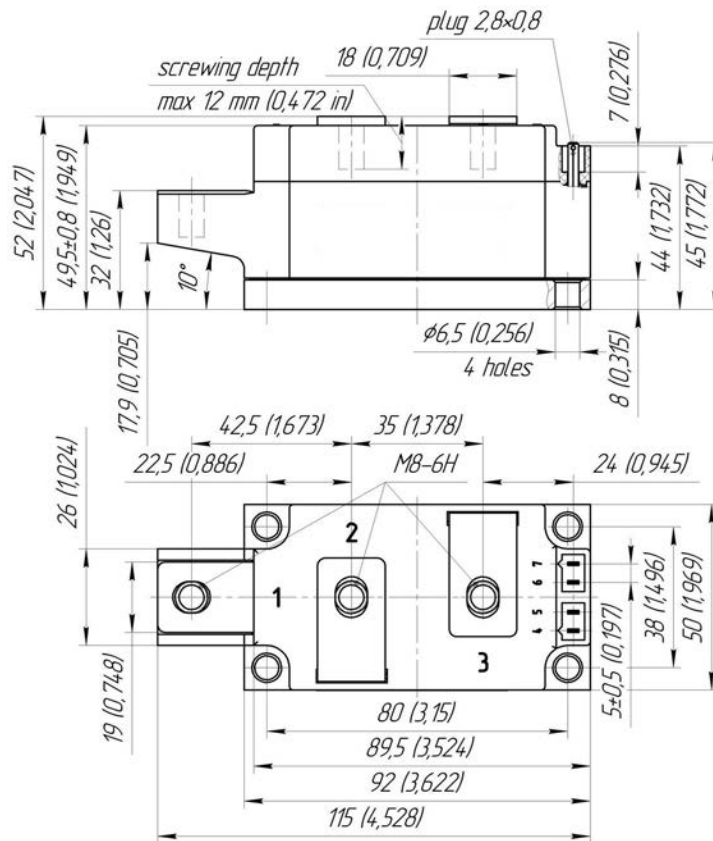
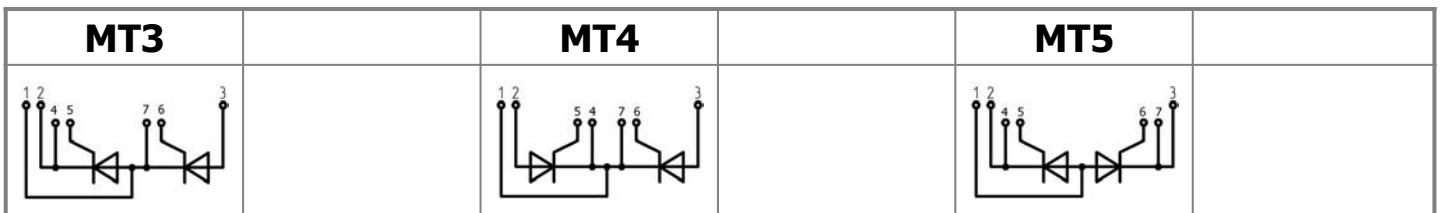


Thyristor / Diode Modules

MT/Dx-200-28-C1, MD/Tx-200-28-C1



Mean on-state current	I_{TAV}	200 A
Repetitive peak off-state voltage	V_{DRM}	2600...2800 V
Repetitive peak reverse voltage	V_{RRM}	
Turn-off time	t_q	250 μ s
V_{DRM}, V_{RRM}, V	2600	2800
Voltage code	26	28
$T_j, ^\circ C$	-40...+125	



MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	Values	Test conditions
ON-STATE				
I_{TAV}	Maximum allowable mean on-state current	A	200 237	$T_c=93\text{ }^\circ\text{C}$; $T_c=85\text{ }^\circ\text{C}$; 180° half-sine wave; 50 Hz
I_{TRMS}	RMS on-state current	A	314	$T_c=93\text{ }^\circ\text{C}$; 180° half-sine wave; 50 Hz
I_{TSM}	Surge on-state current	kA	5.5 6.5	$T_j=T_{j\text{ max}}$ $T_j=25\text{ }^\circ\text{C}$ 180° half-sine wave; $t_p=10\text{ ms}$; single pulse; $V_D=V_R=0\text{ V}$; Gate pulse: $I_G=2\text{ A}$; $t_{GP}=50\text{ }\mu\text{s}$; $di_G/dt \geq 1\text{ A}/\mu\text{s}$
			6.0 7.0	$T_j=T_{j\text{ max}}$ $T_j=25\text{ }^\circ\text{C}$ 180° half-sine wave; $t_p=8.3\text{ ms}$; single pulse; $V_D=V_R=0\text{ V}$; Gate pulse: $I_G=2\text{ A}$; $t_{GP}=50\text{ }\mu\text{s}$; $di_G/dt \geq 1\text{ A}/\mu\text{s}$
I^2t	Safety factor	$\text{A}^2\text{s}\cdot 10^3$	150 210	$T_j=T_{j\text{ max}}$ $T_j=25\text{ }^\circ\text{C}$ 180° half-sine wave; $t_p=10\text{ ms}$; single pulse; $V_D=V_R=0\text{ V}$; Gate pulse: $I_G=2\text{ A}$; $t_{GP}=50\text{ }\mu\text{s}$; $di_G/dt \geq 1\text{ A}/\mu\text{s}$
			145 200	$T_j=T_{j\text{ max}}$ $T_j=25\text{ }^\circ\text{C}$ 180° half-sine wave; $t_p=8.3\text{ ms}$; single pulse; $V_D=V_R=0\text{ V}$; Gate pulse: $I_G=2\text{ A}$; $t_{GP}=50\text{ }\mu\text{s}$; $di_G/dt \geq 1\text{ A}/\mu\text{s}$
BLOCKING				
V_{DRM}, V_{RRM}	Repetitive peak off-state and Repetitive peak reverse voltages	V	2600...2800	$T_{j\text{ min}} < T_j < T_{j\text{ max}}$; 180° half-sine wave; 50 Hz; Gate open
V_{DSM}, V_{RSM}	Non-repetitive peak off-state and Non-repetitive peak reverse voltages	V	2700...2900	$T_{j\text{ min}} < T_j < T_{j\text{ max}}$; 180° half-sine wave; single pulse; Gate open
V_D, V_R	Direct off-state and Direct reverse voltages	V	$0.6\cdot V_{DRM}$ $0.6\cdot V_{RRM}$	$T_j=T_{j\text{ max}}$; Gate open
TRIGGERING				
I_{FGM}	Peak forward gate current	A	6	$T_j=T_{j\text{ max}}$
V_{RGM}	Peak reverse gate voltage	V	5	
P_G	Gate power dissipation	W	3	$T_j=T_{j\text{ max}}$ for DC gate current
SWITCHING				
$(di_T/dt)_{\text{crit}}$	Critical rate of rise of on-state current non-repetitive ($f=1\text{ Hz}$)	$\text{A}/\mu\text{s}$	200	$T_j=T_{j\text{ max}}$; $V_D=0.67\cdot V_{DRM}$; $I_{TM}=2\text{ }I_{TAV}$; Gate pulse: $I_G=2\text{ A}$; $t_{GP}=50\text{ }\mu\text{s}$; $di_G/dt \geq 2\text{ A}/\mu\text{s}$
THERMAL				
T_{stg}	Storage temperature	$^\circ\text{C}$	-40...+50	
T_j	Operating junction temperature	$^\circ\text{C}$	-40...+125	
$T_{\text{c op}}$	Operating temperature	$^\circ\text{C}$	-40...+125	
MECHANICAL				
a	Acceleration under vibration	m/s^2	50	

CHARACTERISTICS

Symbols and parameters		Units	Values	Conditions	
ON-STATE					
V_{TM}	Peak on-state voltage, max	V	2.00	$T_j=25\text{ °C}; I_{TM}=785\text{ A}$	
$V_{T(TO)}$	On-state threshold voltage, max	V	0.90	$T_j=T_{j\text{ max}};$	
r_T	On-state slope resistance, max	m Ω	1.100	$0.5\pi I_{TAV} < I_T < 1.5\pi I_{TAV}$	
I_L	Latching current, max	mA	700	$T_j=25\text{ °C}; V_D=12\text{ V};$ Gate pulse: $I_G=2\text{ A};$ $t_{GP}=50\text{ }\mu\text{s}; di_G/dt\geq 1\text{ A}/\mu\text{s}$	
I_H	Holding current, max	mA	300	$T_j=25\text{ °C};$ $V_D=12\text{ V};$ Gate open	
BLOCKING					
I_{DRM}, I_{RRM}	Repetitive peak off-state and Repetitive peak reverse currents, max	mA	50 2.50	$T_j=T_{j\text{ max}}$ $T_j=25\text{ °C}$	$V_D=V_{DRM}; V_R=V_{RRM}$
$(dv_D/dt)_{crit}$	Critical rate of rise of off-state voltage ¹⁾ , min	V/ μs	1000	$T_j=T_{j\text{ max}};$ $V_D=0.67\cdot V_{DRM};$ Gate open	
TRIGGERING					
V_{GT}	Gate trigger direct voltage, max	V	3.50 2.00 1.50	$T_j=T_{j\text{ min}}$ $T_j=25\text{ °C}$ $T_j=T_{j\text{ max}}$	$V_D=12\text{ V}; I_D=3\text{ A};$ Direct gate current
I_{GT}	Gate trigger direct current, max	mA	250 150 100	$T_j=T_{j\text{ min}}$ $T_j=25\text{ °C}$ $T_j=T_{j\text{ max}}$	
V_{GD}	Gate non-trigger direct voltage, min	V	0.25	$T_j=T_{j\text{ max}};$ $V_D=0.67\cdot V_{DRM};$	
I_{GD}	Gate non-trigger direct current, min	mA	10.00	Direct gate current	
SWITCHING					
t_{gd}	Delay time, max	μs	2.50	$T_j=25\text{ °C}; V_D=1500\text{ V}; I_{TM}=I_{TAV};$ $di/dt=200\text{ A}/\mu\text{s};$ Gate pulse: $I_G=2\text{ A}; V_G=20\text{ V};$ $t_{GP}=50\text{ }\mu\text{s}; di_G/dt=2\text{ A}/\mu\text{s}$	
t_q	Turn-off time ²⁾ , max	μs	250	$dv_D/dt=50\text{ V}/\mu\text{s}; T_j=T_{j\text{ max}}; I_{TM}=I_{TAV};$ $di_R/dt=-10\text{ A}/\mu\text{s}; V_R=100\text{ V};$ $V_D=0.67 V_{DRM}$	
Q_{rr}	Total recovered charge, max	μC	1200	$T_j=T_{j\text{ max}}; I_{TM}=320\text{ A};$	
t_{rr}	Reverse recovery time, max	μs	20	$di_R/dt=-10\text{ A}/\mu\text{s};$	
I_{rr}	Reverse recovery current, max	A	120	$V_R=100\text{ V}$	
THERMAL					
R_{thjc}	Thermal resistance, junction to case			180° half-sine wave, 50 Hz	
	per module	$^{\circ}\text{C}/\text{W}$	0.0550		
	per arm	$^{\circ}\text{C}/\text{W}$	0.1100		
R_{thch}	Thermal resistance, case to heatsink				
	per module	$^{\circ}\text{C}/\text{W}$	0.0200		
	per arm	$^{\circ}\text{C}/\text{W}$	0.0400		
INSULATION					
V_{ISOL}	Insulation test voltage	kV	3.00	Sine wave, 50 Hz; RMS	t=60 sec
			3.60		t=1 sec
MECHANICAL					
M_1	Mounting torque (M6) ³⁾	Nm	6.00	Tolerance $\pm 15\%$	
M_2	Terminal connection torque (M8) ³⁾	Nm	9.00	Tolerance $\pm 15\%$	
m	Weight, max	g	860		

PART NUMBERING GUIDE	NOTES								
<p>MT 3 - 200 - 28 - A2 M2 - C1 - N 1 2 3 4 5 6 7 8</p> <p>1. Thyristor module (MT) Thyristor – Diode module (MT/D) Diode – Thyristor module (MD/T)</p> <p>2. Circuit Schematic</p> <p>3. Average On-state Current, A</p> <p>4. Voltage Code</p> <p>5. Critical rate of rise of off-state voltage</p> <p>6. Group of turn-off time ($dv_D/dt=50\text{ V}/\mu\text{s}$)</p> <p>7. Package Type (M.C1)</p> <p>8. Ambient Conditions: N – Normal</p>	<p>1) Critical rate of rise of off-state voltage</p> <table border="1" data-bbox="831 152 1511 226"> <tr> <td>Symbol of group</td> <td>A2</td> </tr> <tr> <td>$(dv_D/dt)_{crit}, \text{ V}/\mu\text{s}$</td> <td>1000</td> </tr> </table> <p>2) Turn-off time ($dv_D/dt=50\text{ V}/\mu\text{s}$)</p> <table border="1" data-bbox="831 286 1511 360"> <tr> <td>Symbol of group</td> <td>M2</td> </tr> <tr> <td>$t_q, \mu\text{s}$</td> <td>250</td> </tr> </table> <p>3) The screws must be lubricated</p>	Symbol of group	A2	$(dv_D/dt)_{crit}, \text{ V}/\mu\text{s}$	1000	Symbol of group	M2	$t_q, \mu\text{s}$	250
Symbol of group	A2								
$(dv_D/dt)_{crit}, \text{ V}/\mu\text{s}$	1000								
Symbol of group	M2								
$t_q, \mu\text{s}$	250								

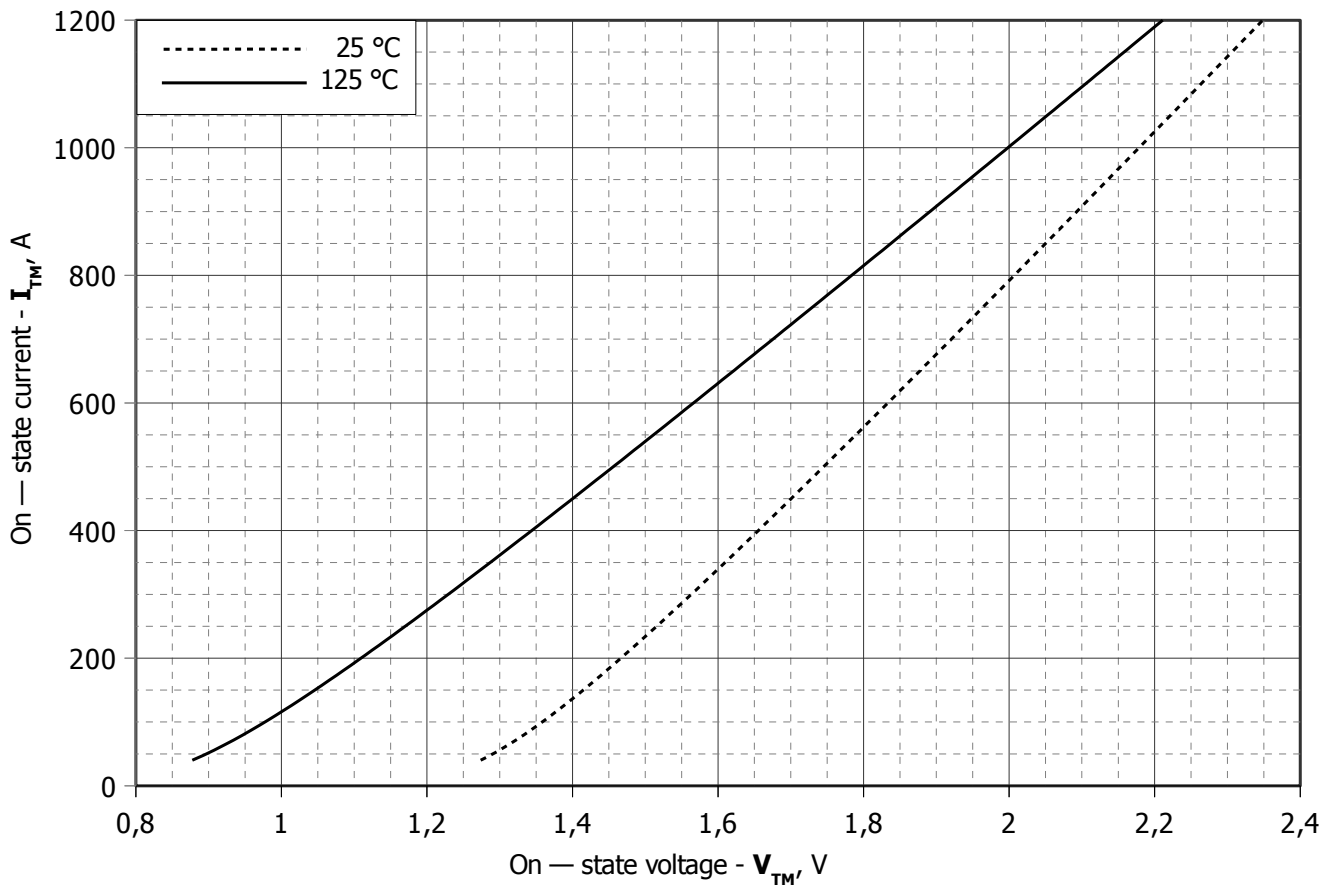


Fig 1 – On-state characteristics of Limit device

Analytical function for On-state characteristic:

$$V_T = A + B \cdot i_T + C \cdot \ln(i_T + 1) + D \cdot \sqrt{i_T}$$

	Coefficients for max curves	
	$T_j = 25^\circ\text{C}$	$T_j = T_{j,\text{max}}$
A	1.08580000	0.67849000
B	0.00082840	0.00102550
C	0.04373700	0.04254400
D	-0.00119770	0.00001079

On-state characteristic model (see Fig. 1)

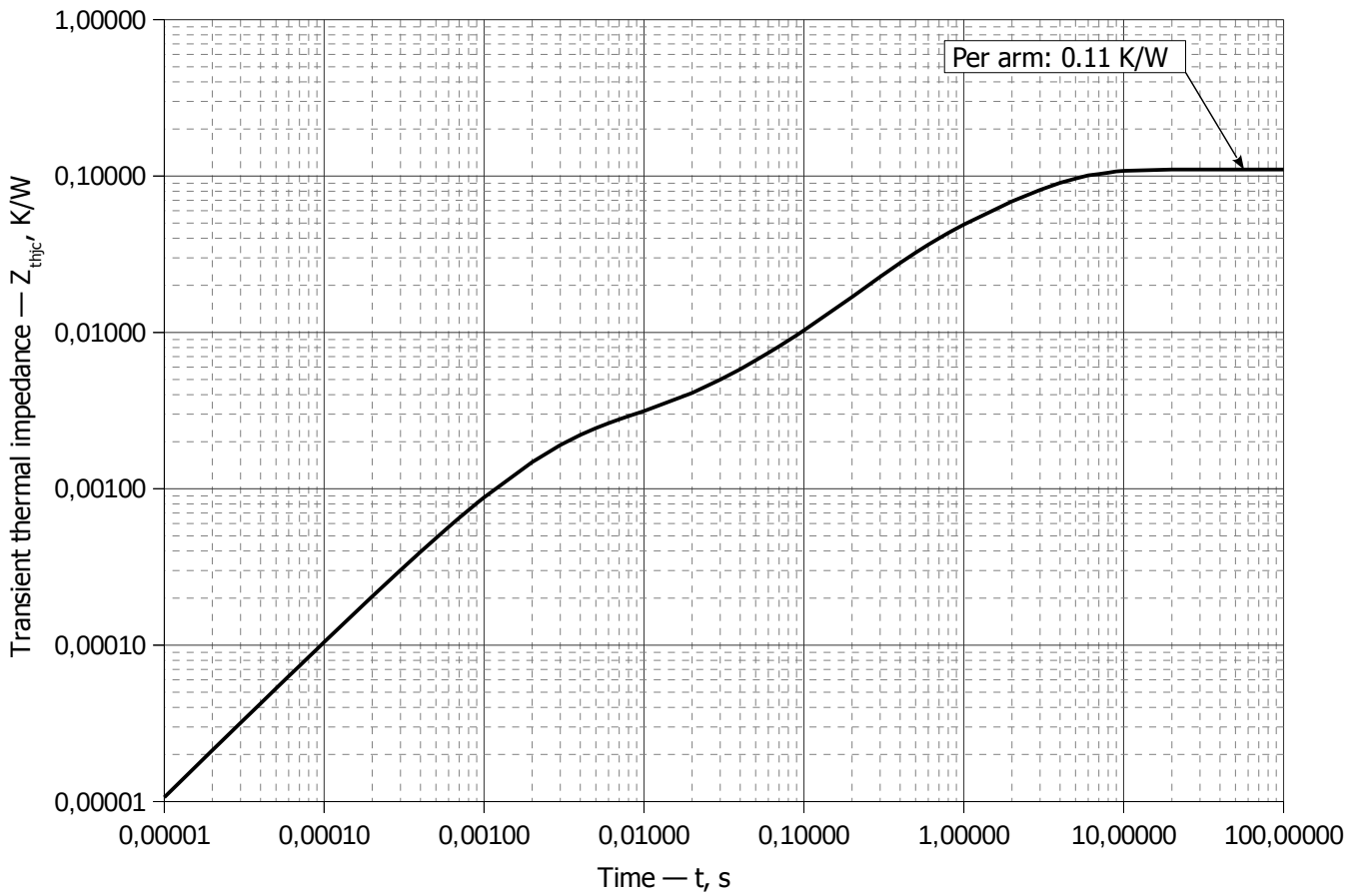


Fig 2 – Transient thermal impedance Z_{thjc} vs. time t

Analytical function for Transient thermal impedance junction to case Z_{thjc} for DC:

$$Z_{thjc} = \sum_{i=1}^n R_i \left(1 - e^{-\frac{t}{\tau_i}} \right)$$

Where $i = 1$ to n , n is the number of terms in the series.

t = Duration of heating pulse in seconds.

Z_{thjc} = Thermal resistance at time t .

R_i = Amplitude of p_{th} term.

τ_i = Time constant of r_{th} term.

i	1	2	3	4	5	6
R_i K/W	0.0808	0.007806	0.02226	-0.007688	0.00471	0.00217
τ_i s	2.801	1.283	0.3281	0.09408	0.0572	0.002255

Transient thermal impedance junction to case Z_{thjc} model (see Fig. 2)

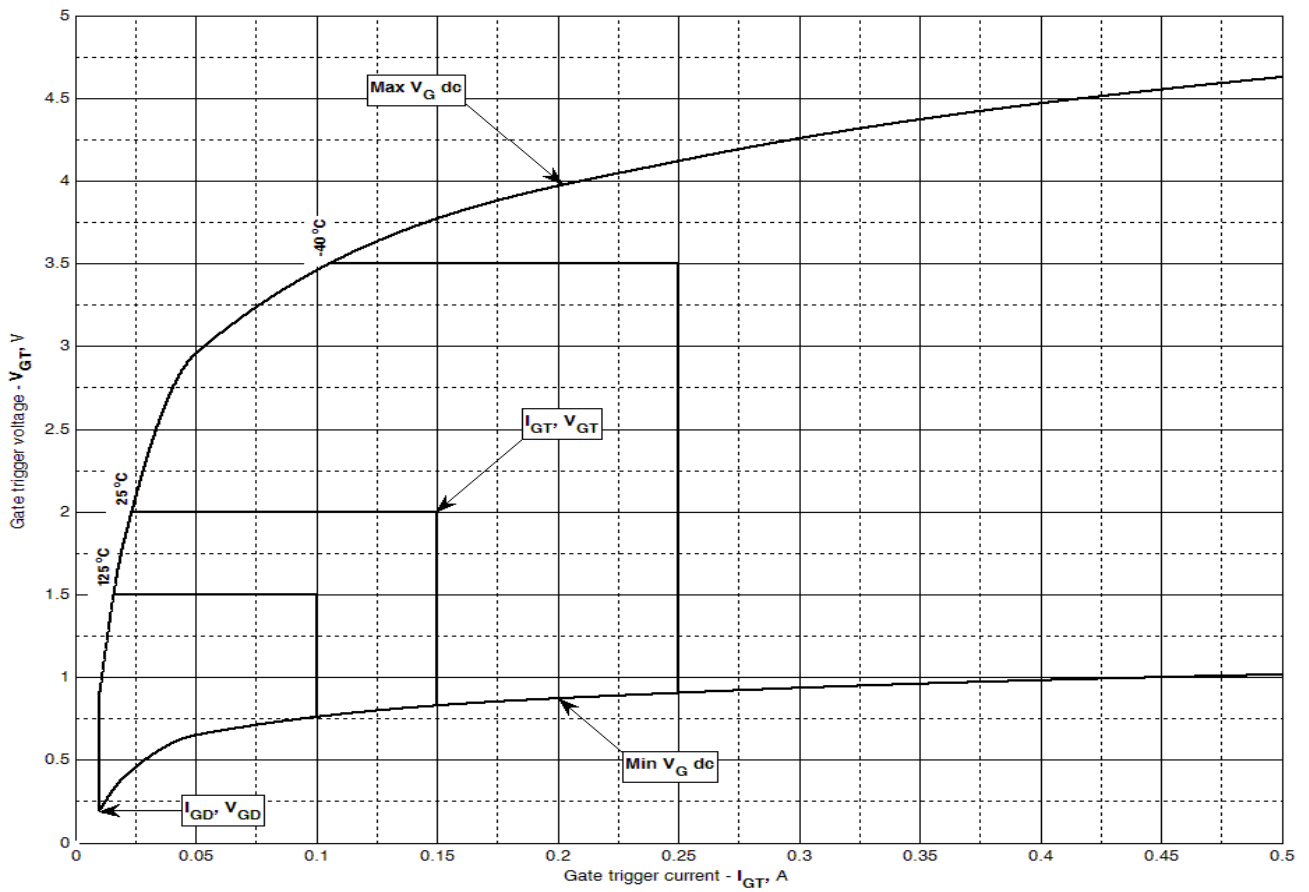


Fig 3 – Gate characteristics – Trigger limits

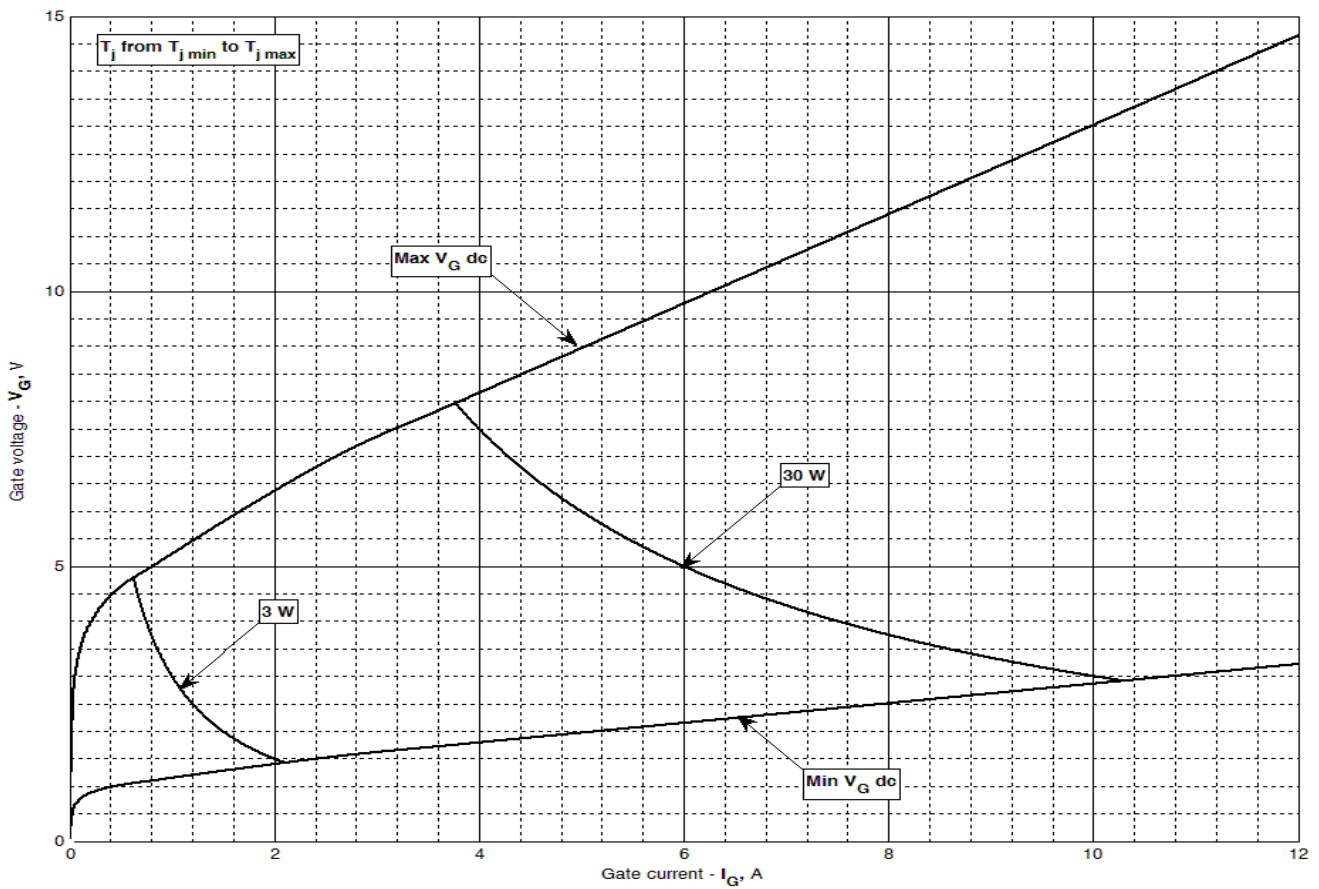


Fig 4 - Gate characteristics – Power curves

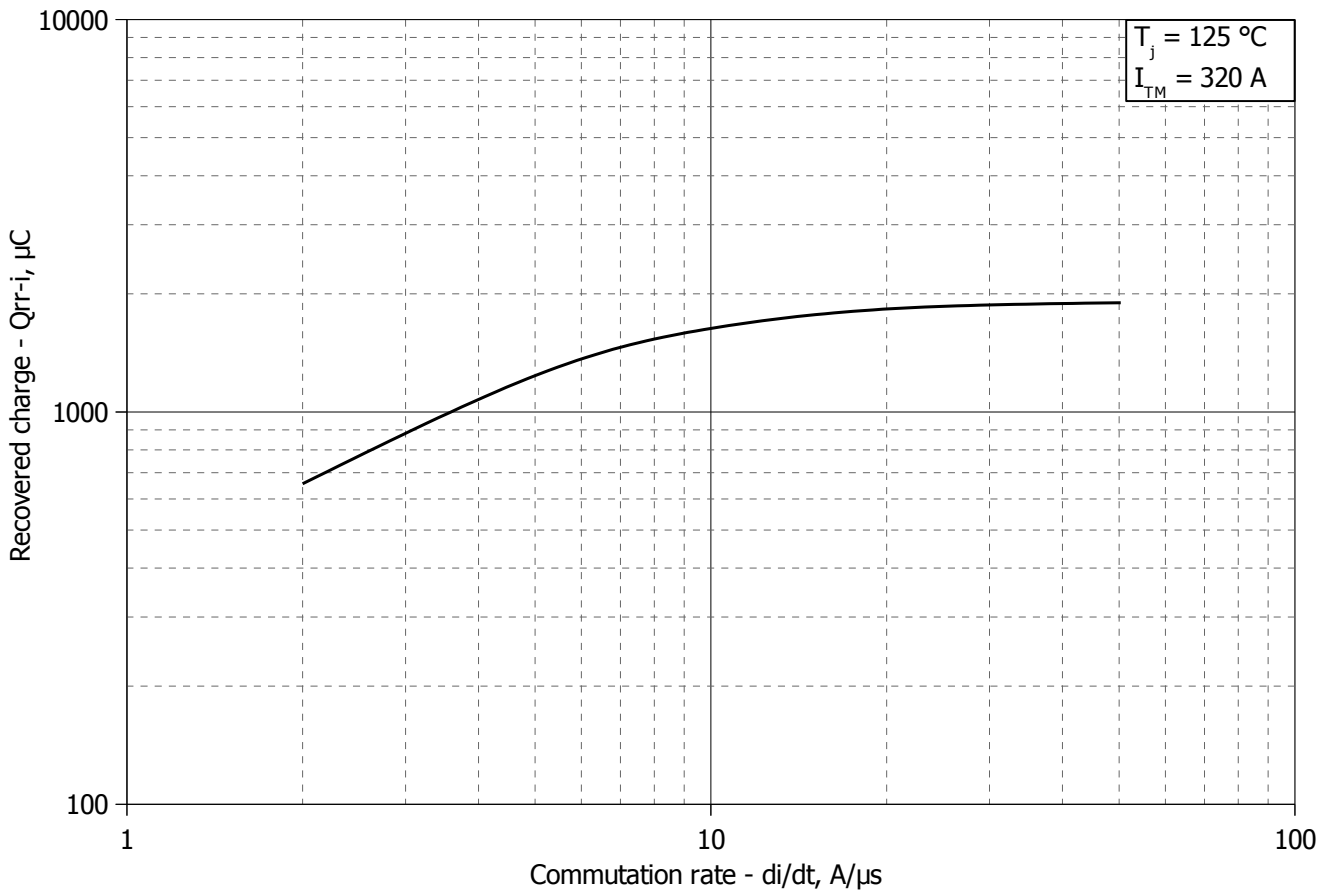


Fig 5 – Maximum recovered charge Q_{rr-i} (integral) vs. commutation rate di_R/dt

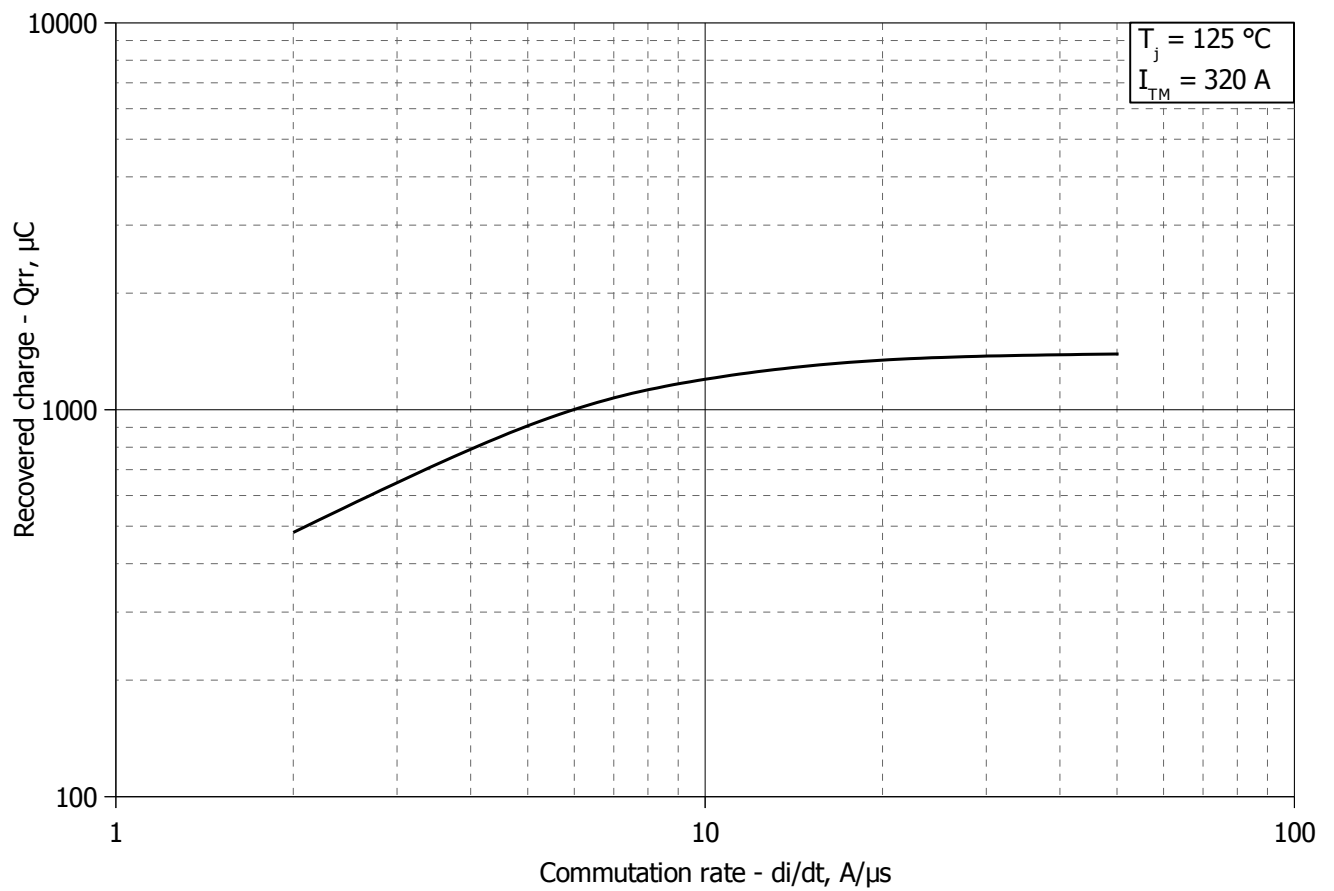


Fig 6 – Maximum recovered charge Q_{rr} vs. commutation rate di_R/dt (25% chord)

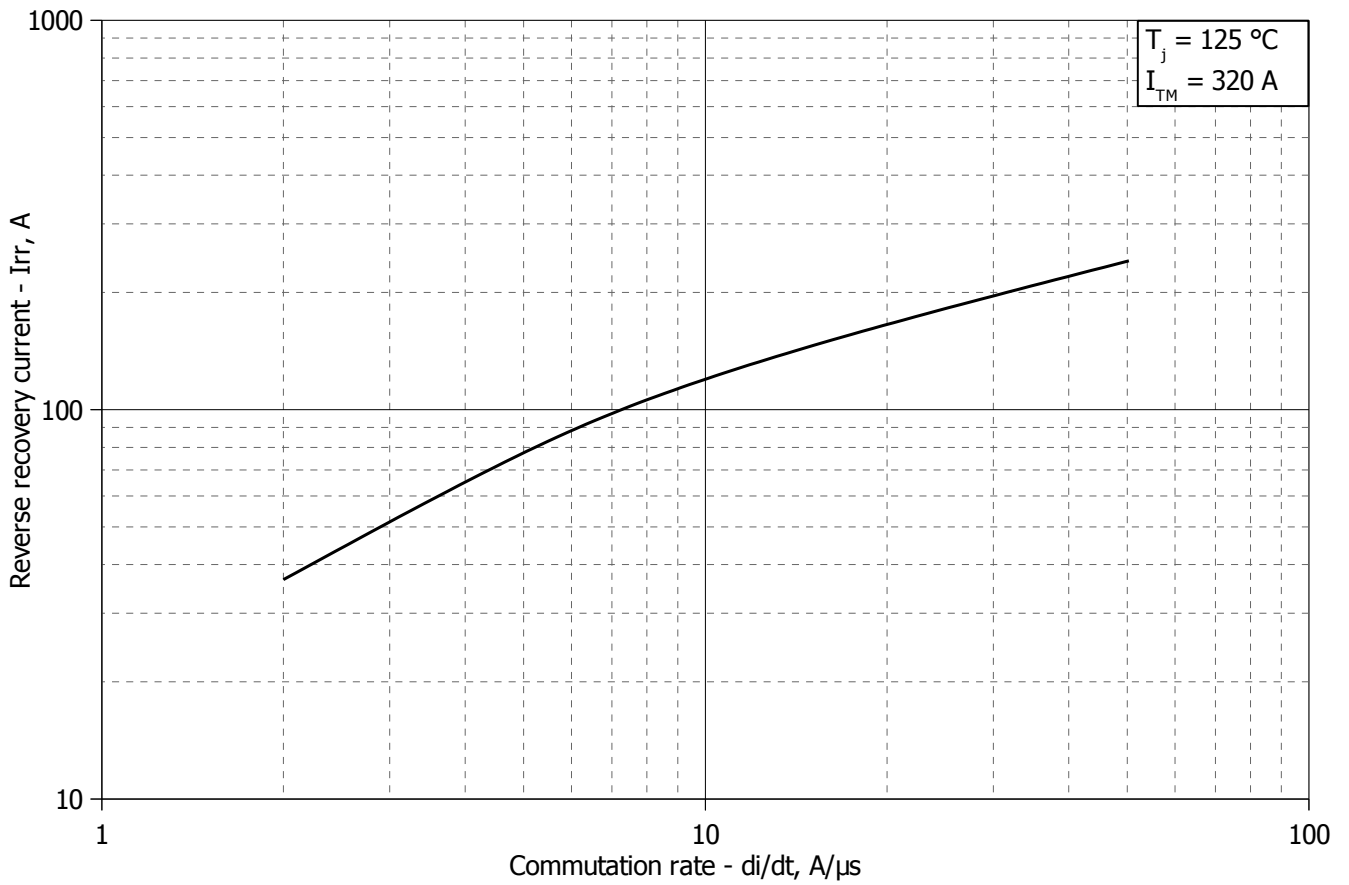


Fig 7 – Maximum reverse recovery current I_{rr} vs. commutation rate di_R/dt

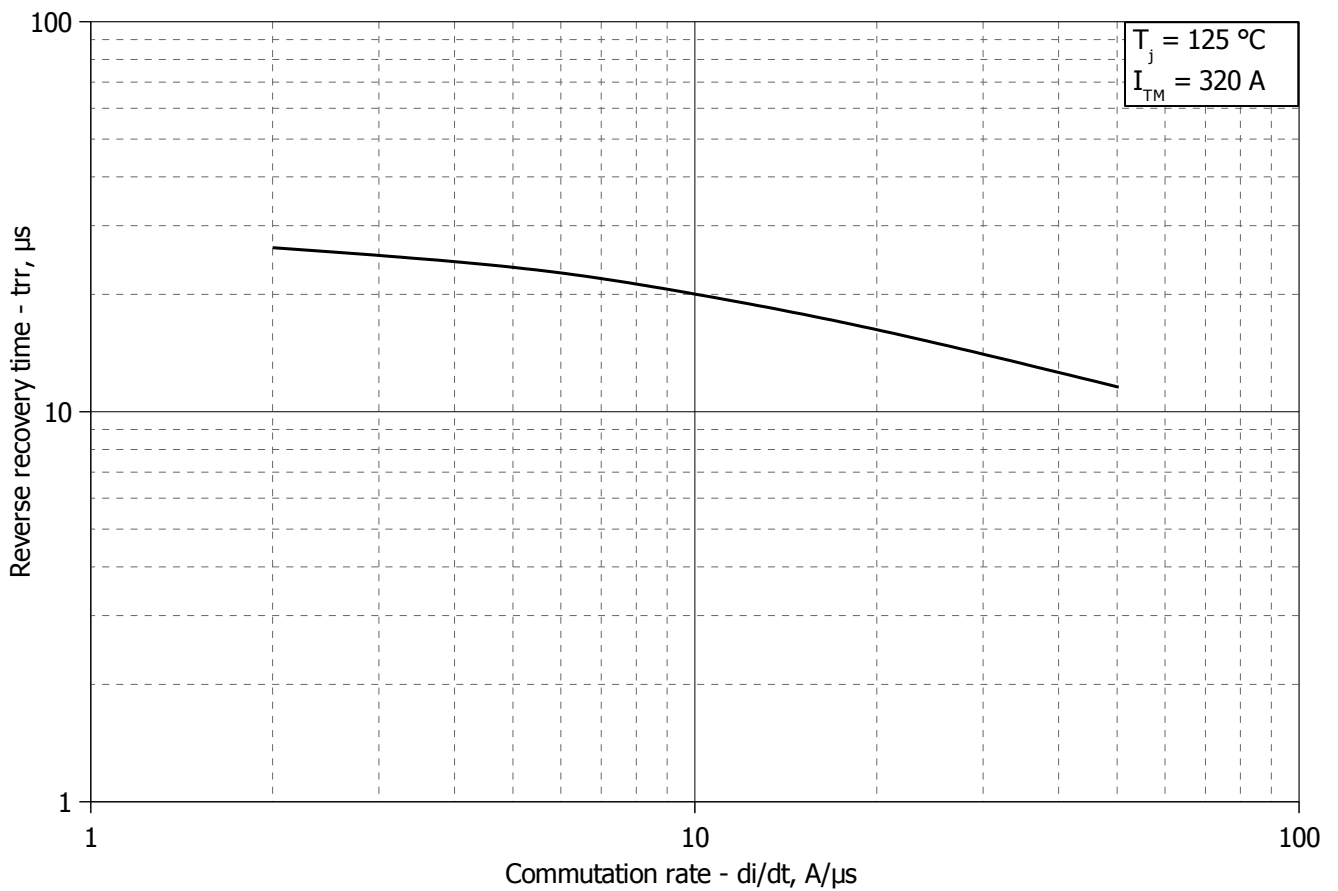


Fig 8 – Maximum recovery time t_{rr} vs. commutation rate di_R/dt (25% chord)

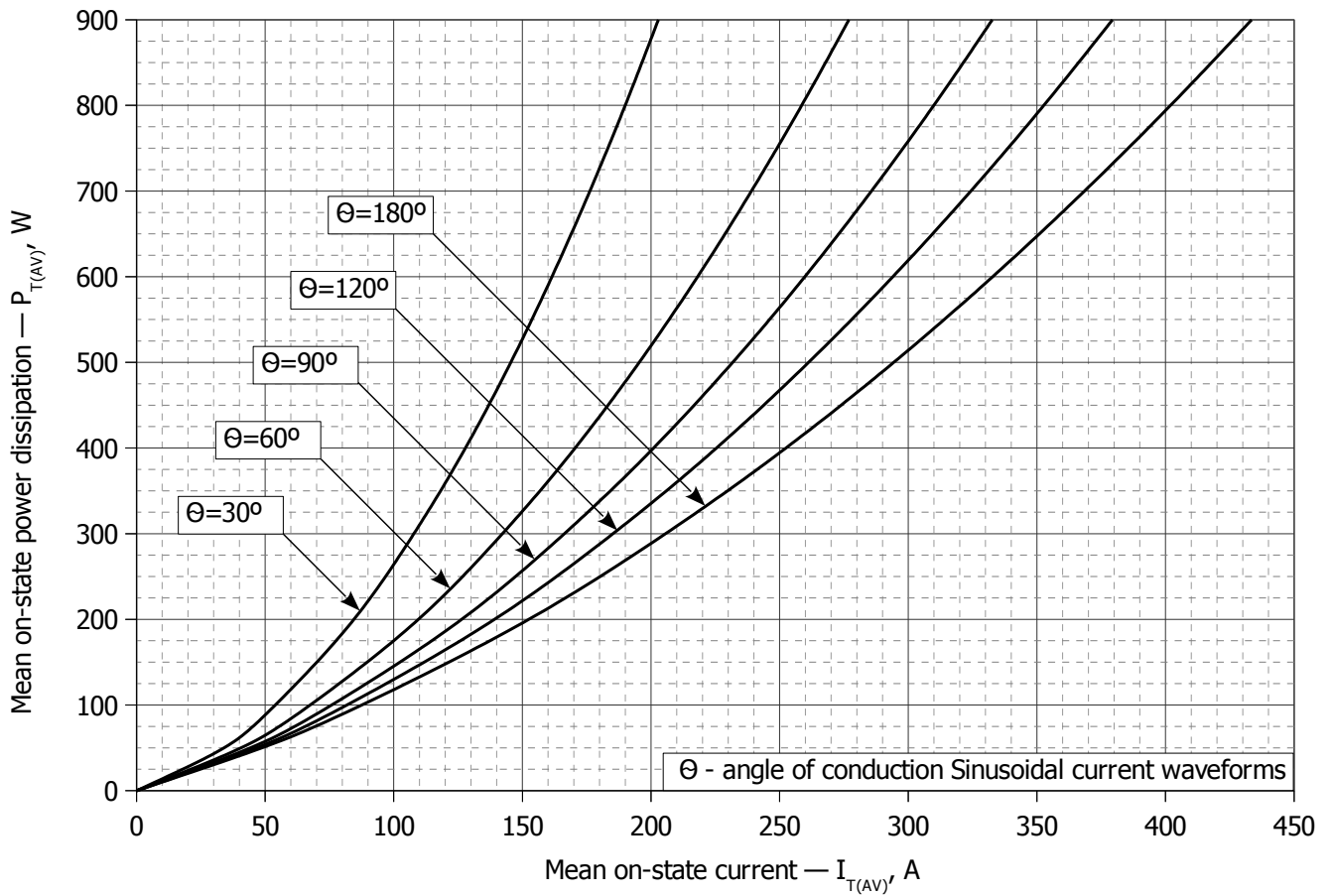


Fig. 9 - Mean on-state power dissipation P_{TAV} vs. mean on-state current I_{TAV} for sinusoidal current waveforms at different conduction angles ($f=50\text{Hz}$, DSC)

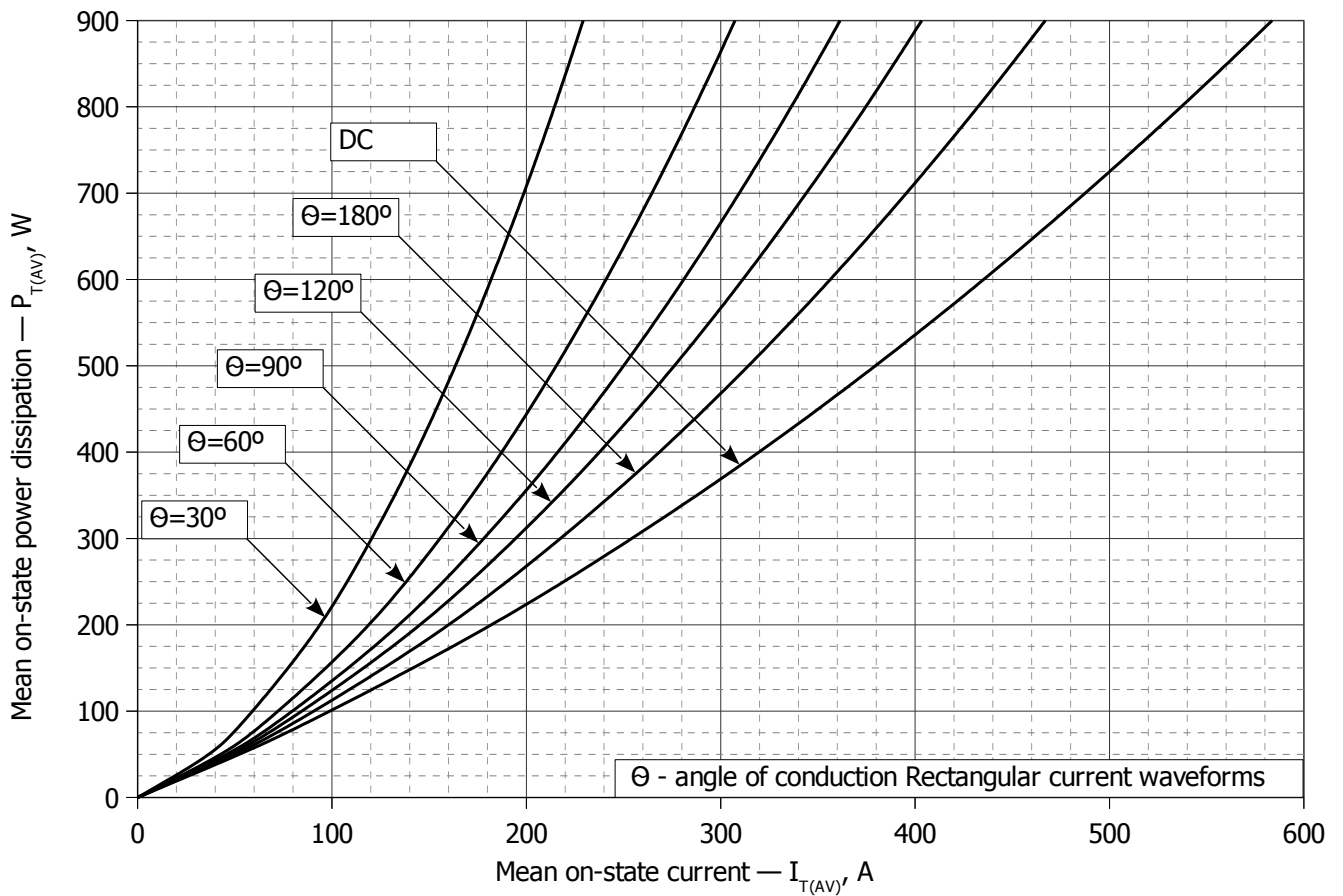


Fig. 10 - Mean on-state power dissipation P_{TAV} vs. mean on-state current I_{TAV} for rectangular current waveforms at different conduction angles and for DC ($f=50\text{Hz}$, DSC)

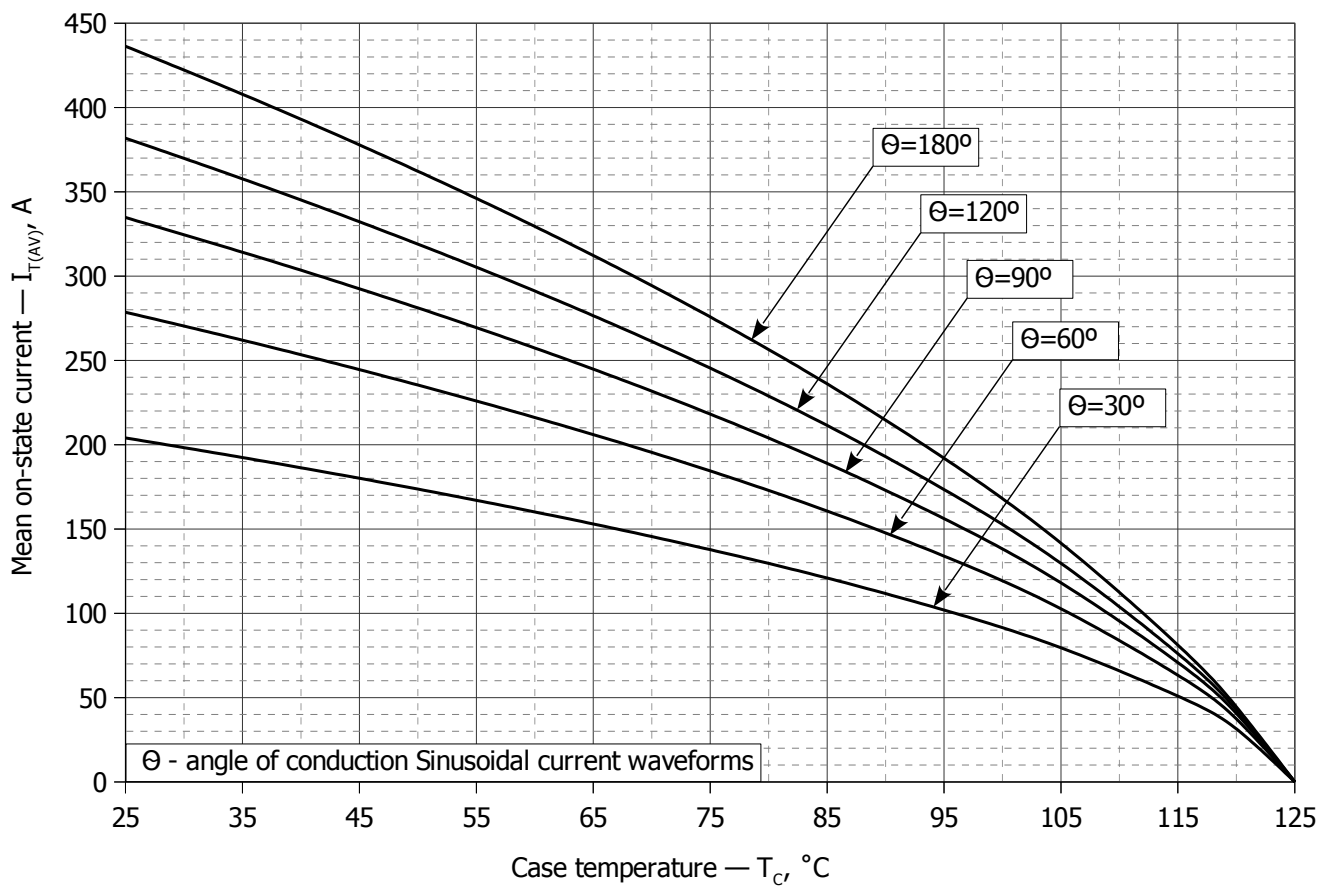


Fig. 11 – Mean on-state current I_{TAV} vs. case temperature T_c for sinusoidal current waveforms at different conduction angles ($f=50\text{Hz}$, DSC)

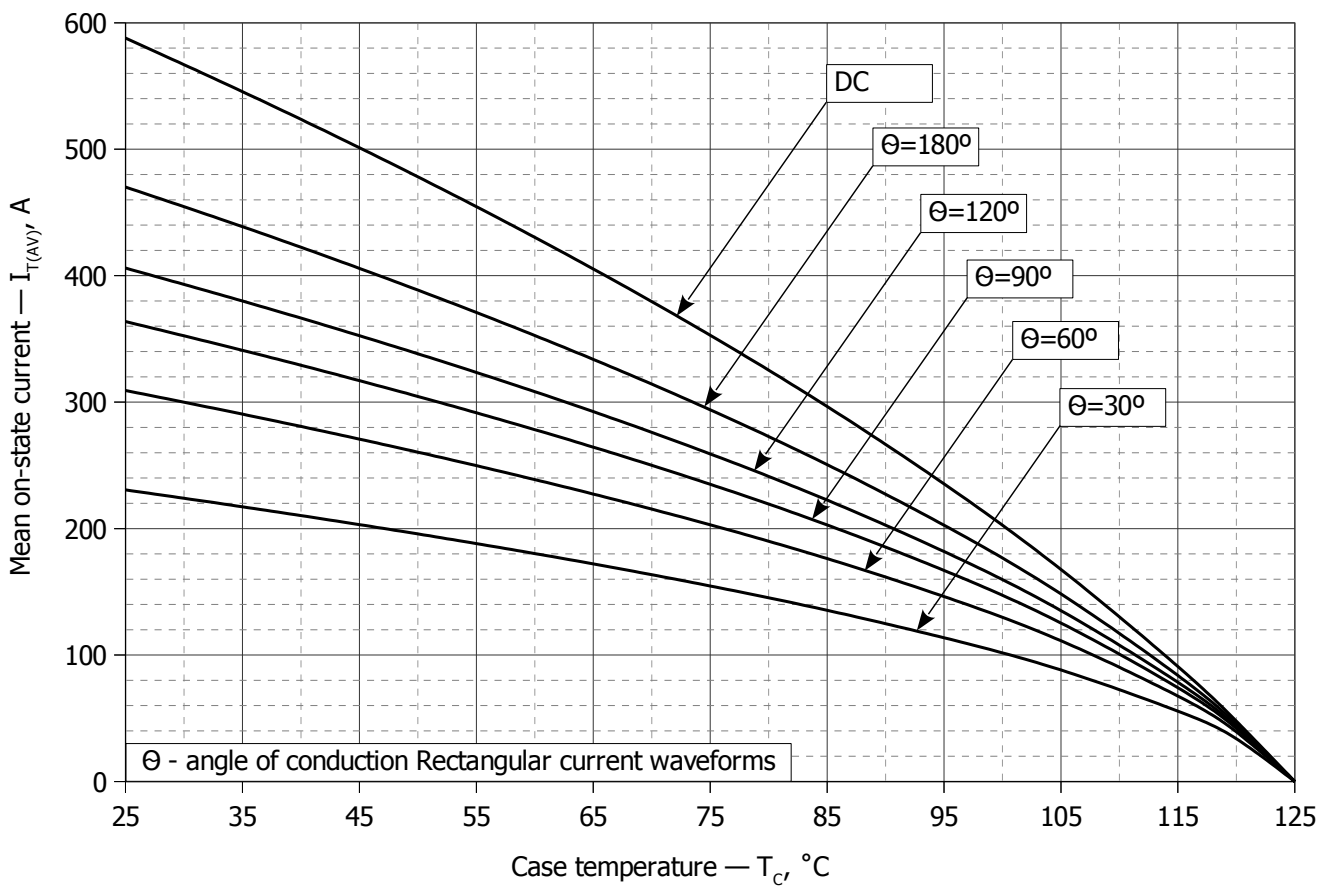


Fig. 12 - Mean on-state current I_{TAV} vs. case temperature T_c for rectangular current waveforms at different conduction angles and for DC ($f=50\text{Hz}$, DSC)

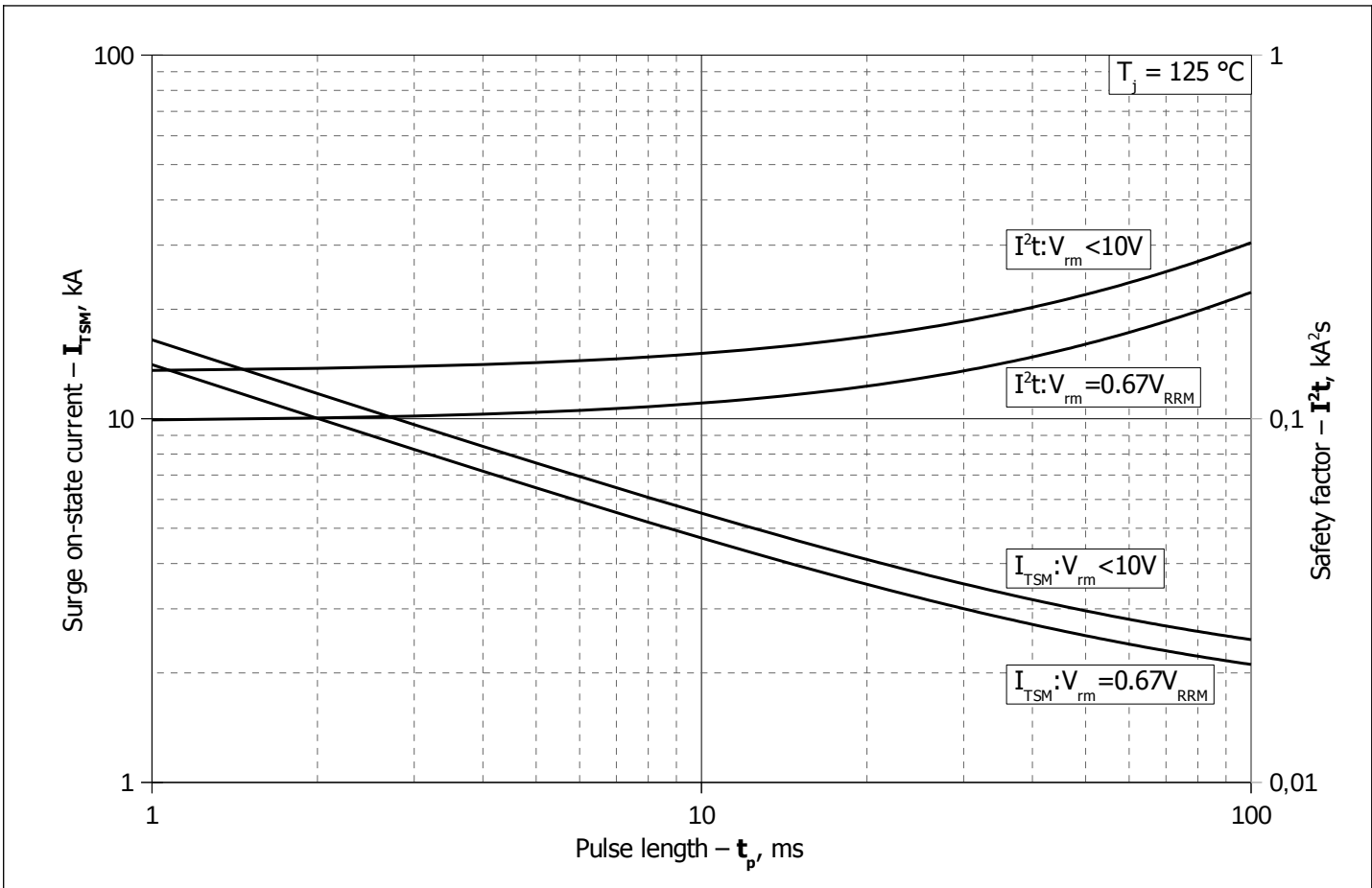


Fig. 13 – Maximum surge on-state current I_{TSM} and safety factor I^2t vs. pulse length t_p

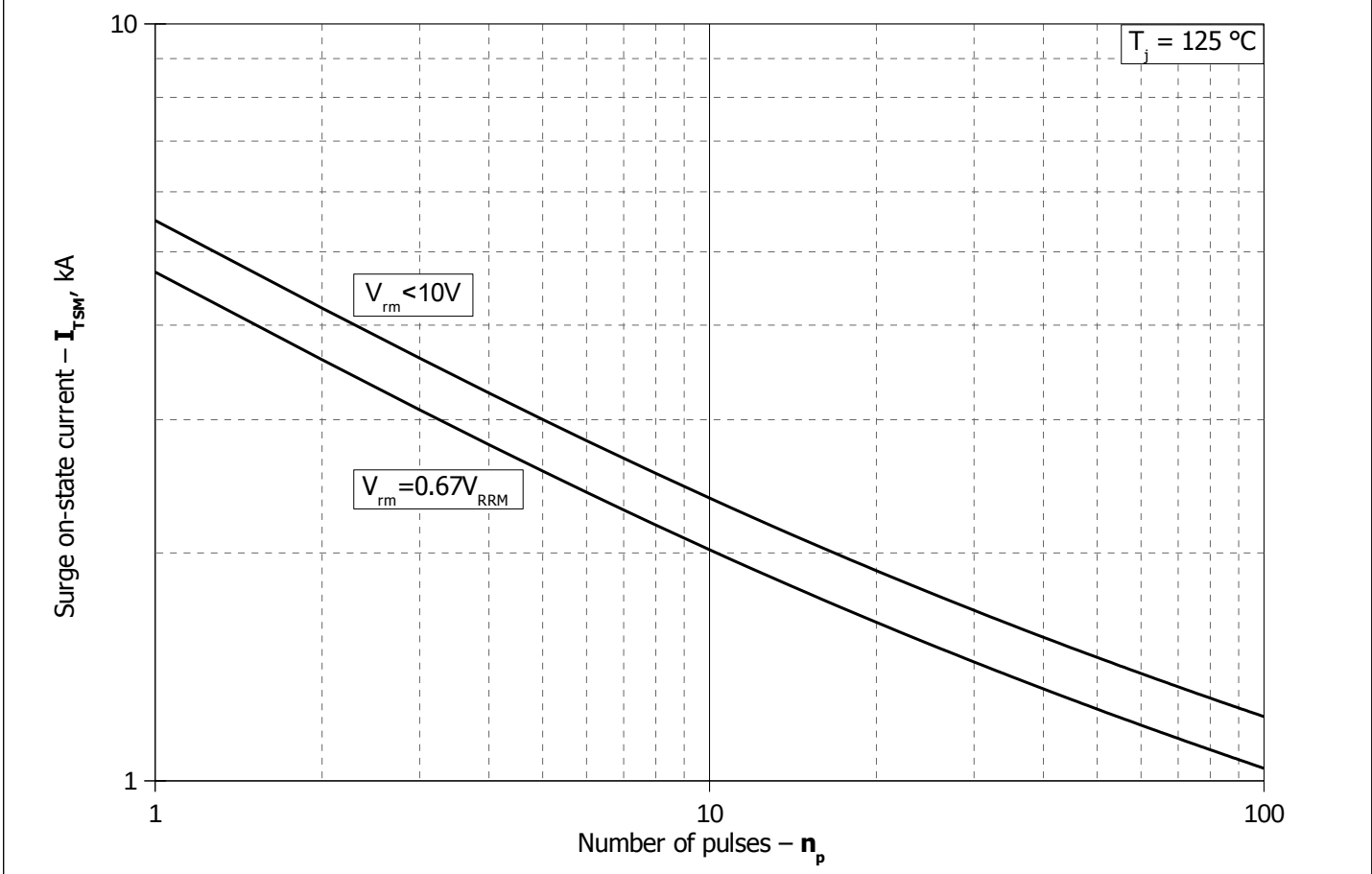


Fig. 14 - Maximum surge on-state current I_{TSM} vs. number of pulses n_p