

## Rectifier Diode Modules AMKE 1201-22



Symbols and parameters			Values	Units
<b>I<sub>FAV</sub></b>	Mean forward current	sin 180; T <sub>C</sub> = 85 (100)°C	1360 (1100)	A
<b>I<sub>FRMS</sub></b>	RMS forward current	continuous operation	1700	A
<b>V<sub>RSM</sub></b>	Non-repetitive peak reverse voltage		2300	V
<b>V<sub>RRM</sub></b>	Repetitive peak reverse voltage		2200	V
<b>I<sub>FSM</sub></b>	Surge forward current	T <sub>j</sub> = 25°C; 10 ms T <sub>j</sub> = 150°C; 10 ms	41000 35000	A A
<b>i<sup>2</sup>t</b>	i <sup>2</sup> t value, rating for fusing	T <sub>j</sub> = 25°C; 10 ms T <sub>j</sub> = 150°C; 10 ms	8405000 6125000	A <sup>2</sup> s A <sup>2</sup> s
<b>V<sub>F</sub></b>	Forward voltage	T <sub>j</sub> = 150°C; I <sub>F</sub> = 3000 A	max. 1.11	V
<b>V<sub>(TO)</sub></b>	On-state threshold voltage	T <sub>j</sub> = 150°C	max. 0.75	V
<b>r<sub>T</sub></b>	On-state slope resistance	T <sub>j</sub> = 150°C	max. 0.073	mΩ
<b>I<sub>RD</sub></b>	Direct reverse current	T <sub>j</sub> = 150°C, V <sub>RD</sub> = V <sub>RRM</sub>	max. 150	mA
<b>R<sub>th(j-c)</sub></b>	Thermal resistance, junction to case	cont.; per chip / per module sin.180°; per chip / per module	0.0466 0.048	K/W K/W
<b>R<sub>th(c-s)</sub></b>	Thermal resistance, junction to heatsink	per chip / per module	0.015	K/W
<b>T<sub>j</sub></b>	Junction temperature		-40 ... +150	°C
<b>T<sub>stg</sub></b>	Storage temperature range		-40 ... +150	°C
<b>V<sub>isol</sub></b>	Insulation test voltage (r.m.s.)	a.c. 50 Hz; r.m.s.; 1s / 1min.	3600 / 3000	V
<b>M<sub>s</sub></b>	Mounting torque on heatsink	min / max	5.1 / 6.9	Nm
<b>M<sub>t</sub></b>	Mounting torque for terminals	min / max	16.2 / 19.8	Nm
<b>a</b>	Maximum allowable acceleration		5*9.81	m/s <sup>2</sup>
<b>W</b>	Weight		1950	g

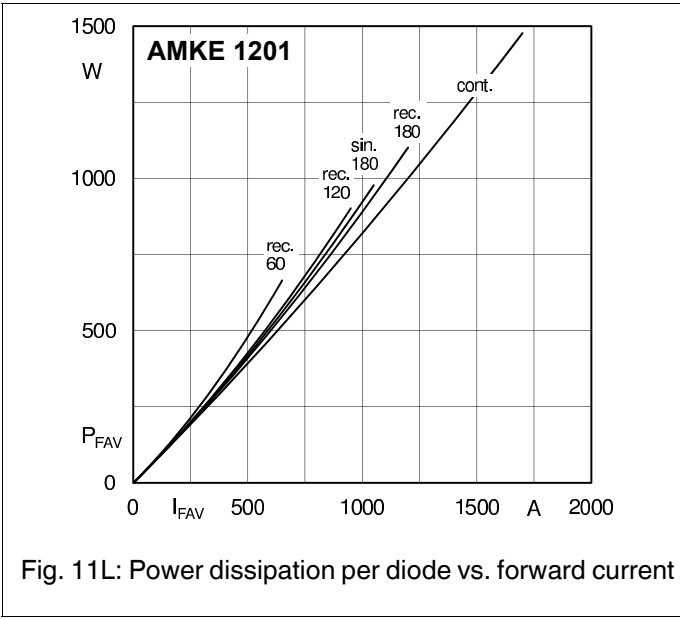


Fig. 11L: Power dissipation per diode vs. forward current

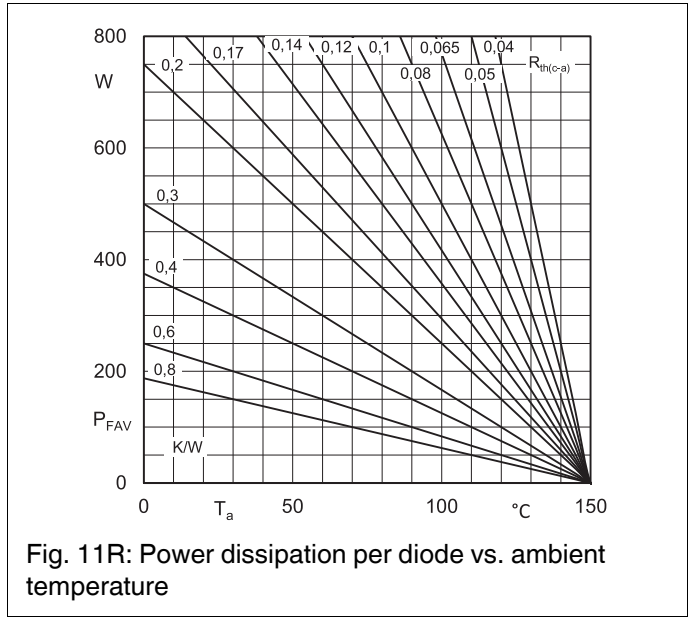


Fig. 11R: Power dissipation per diode vs. ambient temperature

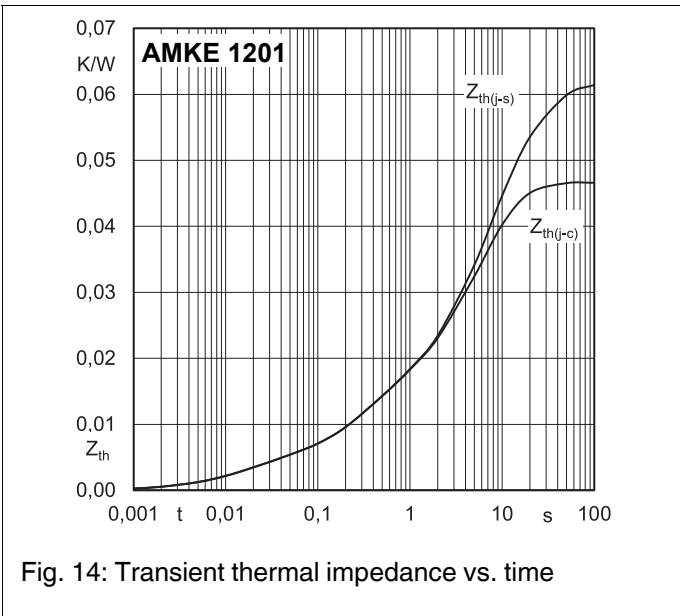


Fig. 14: Transient thermal impedance vs. time

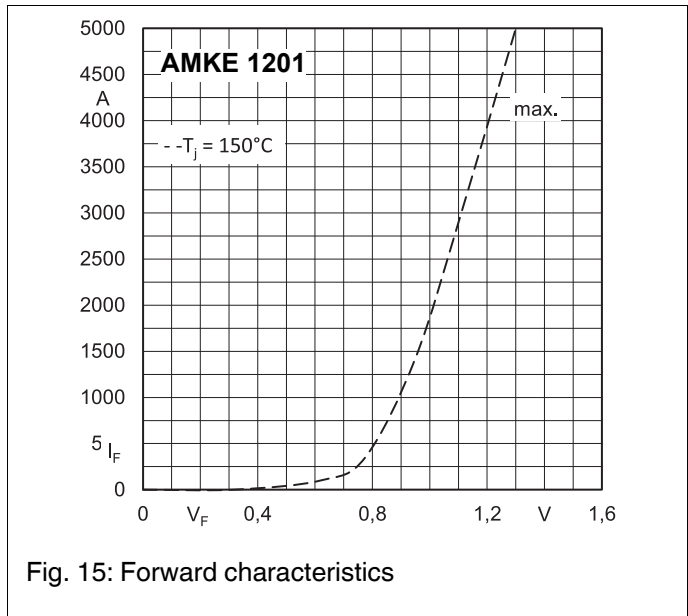


Fig. 15: Forward characteristics

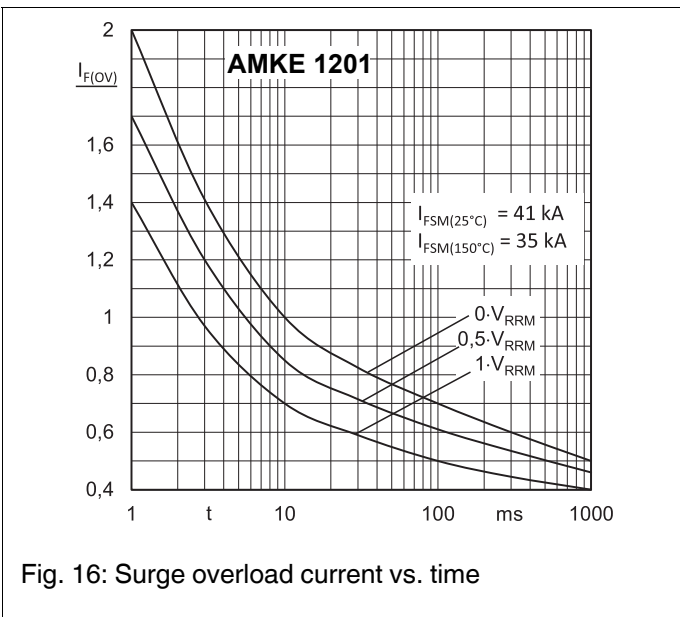
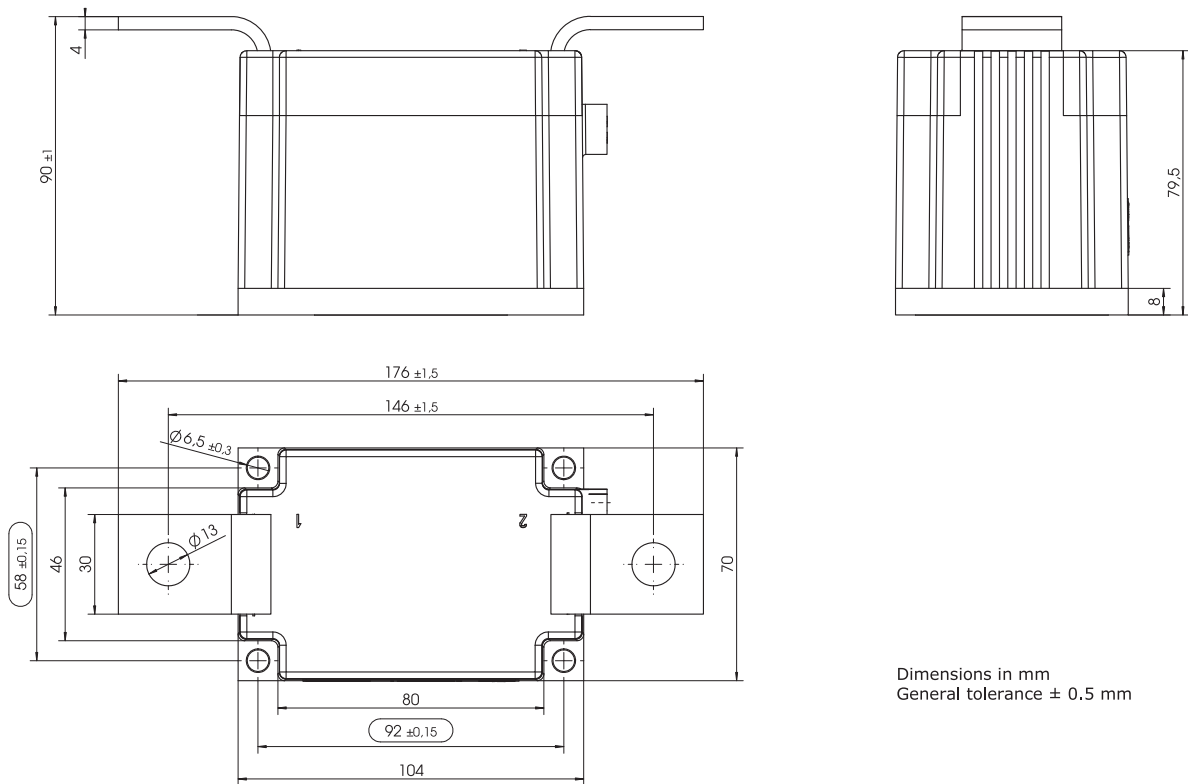


Fig. 16: Surge overload current vs. time

## DIMENSIONS



## TOPOLOGY OF INTERNAL CONNECTION

