



Diode Module

AMDD600N12K



Key Parameters

$I_{F(AV)M}$	=	600	A
V_{RRM}	=	1200	V
I_{FSM}	=	22000	A
V_{TO}	=	0.75	V
r_T	=	0.215	$\text{m}\Omega$

Properties

- International standard package
- High operation reliability
- Electrically insulated base plate

MAXIMUM ALLOWABLE RATINGS

Symbols and parameters			Maximum Limits	Unit
V_{RRM}	Repetitive peak reverse voltage	$T_{vj} = -40^\circ\text{C} \dots T_{vj \text{ max}}$	1200	V
V_{RSM}	Non-repetitive peak reverse voltage	$T_{vj} = +25^\circ\text{C} \dots T_{vj \text{ max}}$	1300	V
I_{FAVM}	Average on-state current	$T_C = 100^\circ\text{C}$	600	A
I_{FRMSM}	Maximum RMS on-state current		950	A
I_{FSM}	Surge current	$T_{vj} = 25^\circ\text{C}, t_p = 10 \text{ ms}$ $T_{vj} = T_{vj \text{ max}}, t_p = 10 \text{ ms}$	22000 19000	A A
I^2t	Safety factor	$T_{vj} = 25^\circ\text{C}, t_p = 10 \text{ ms}$ $T_{vj} = T_{vj \text{ max}}, t_p = 10 \text{ ms}$	2420000 1800000	A^2s A^2s

CHARACTERISTICS

Symbols and parameters			Value			Unit
			min	typ	max	
V_F	On-state voltage	T _{vj} = T _{vj max} , I _F = 1800 A			1.32	V
V_(TO)	Threshold voltage	T _{vj} = T _{vj max}			0.75	V
r_T	Slope resistance	T _{vj} = T _{vj max}			0.215	mΩ
I_R	Reverse current	T _{vj} = T _{vj max} , V _R = V _{RRM}			40	mA
V_{ISOL}	Insulation test voltage	RMS, f = 50Hz, t = 1 sec RMS, f = 50Hz, t = 1 min			3.6 3.0	kV

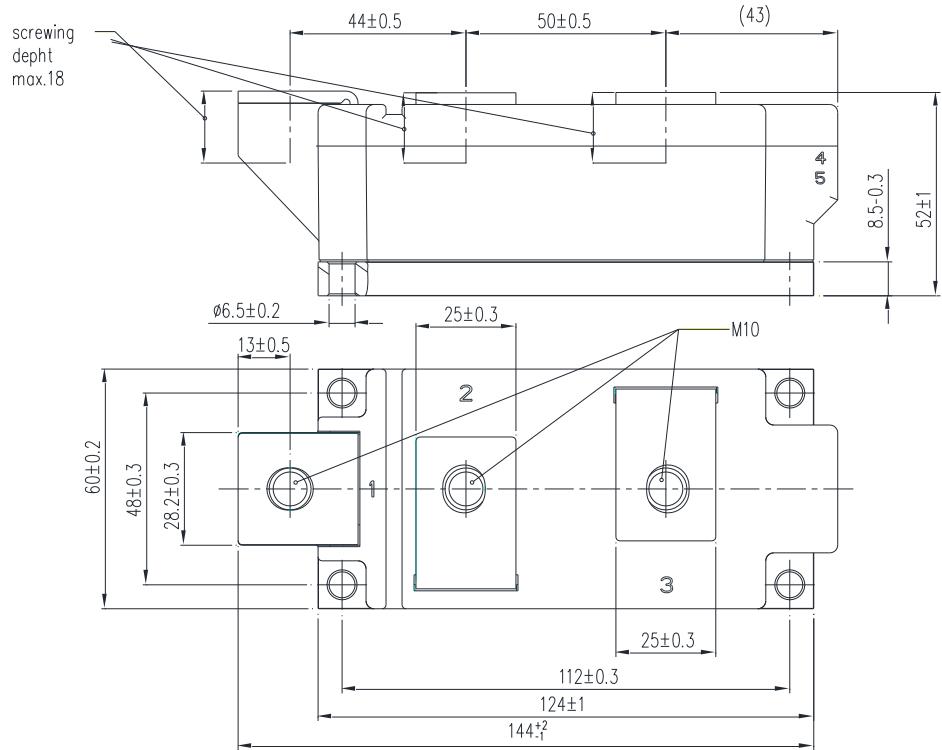
THERMAL PARAMETERS

Symbols and parameters			Value	Unit
R_{th(j-c)}	Thermal resistance, junction to case	per Module, θ = 180° sin per arm, θ = 180° sin per Module, DC per arm, DC	0.0390 0.0780 0.0373 0.0745	K/W
R_{th(c-h)}	Thermal resistance, case to heatsink	per Module per arm	0.01 0.02	K/W
T_{vj max}	Maximum junction temperature		+150	°C
T_{C op}	Operating temperature range		-40...+150	°C
T_{stg}	Storage temperature range		-40...+150	°C

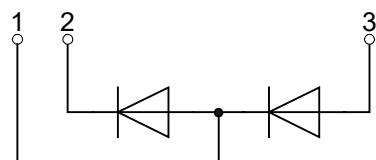
MECHANICAL PARAMETERS

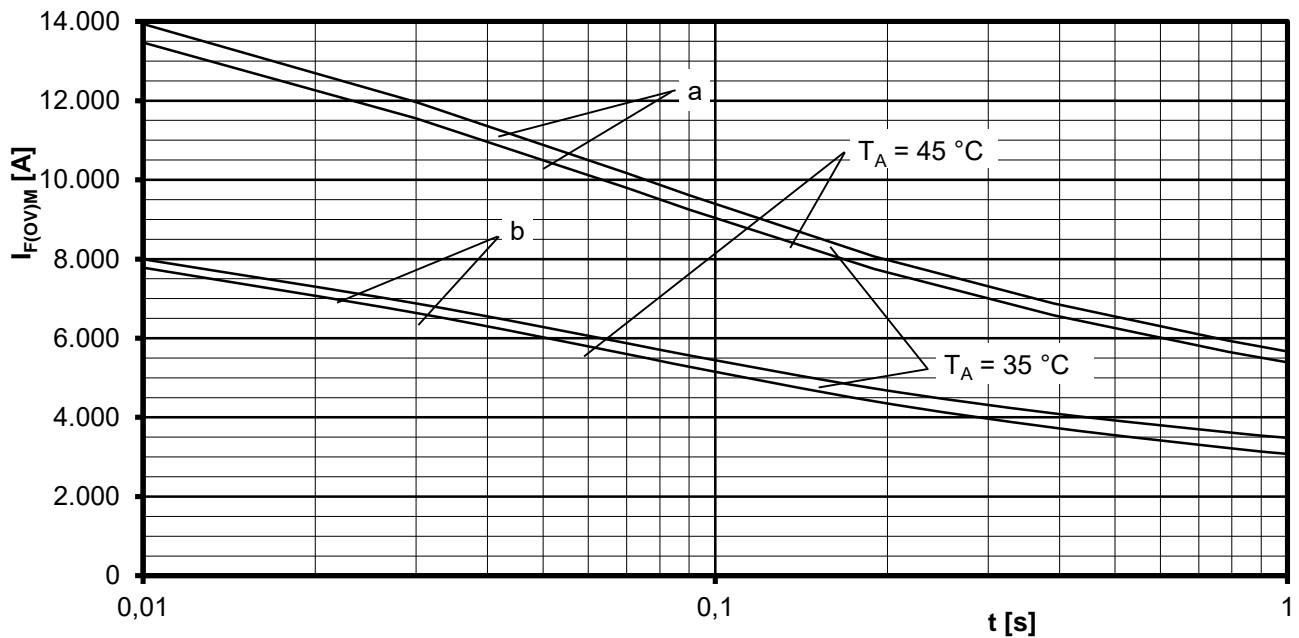
Symbols and parameters			Value	Unit
M1	Mounting torque	Tolerance ± 15%	6	Nm
M2	Terminal connection torque	Tolerance ± 10%	12	Nm
W	Weight		1500	g
a	Vibration resistance	f = 50 Hz	50	m/s ²

DIMENSIONS



TOPOLOGY OF INTERNAL CONNECTION



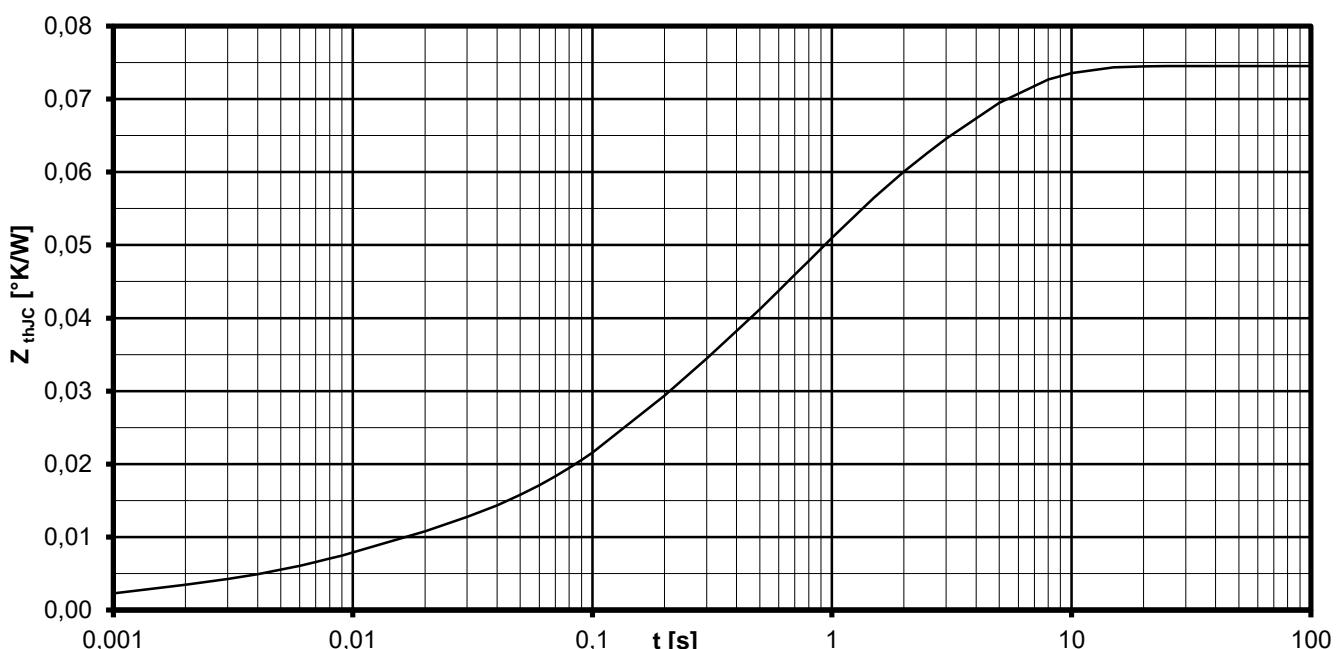


Maximum overload on-state current per arm $I_{F(OV)M} = f(t)$, $v_{RM} = 0,8 V_{RRM}$

a: No-load conditions
 b: Pre-load current per arm $I_{FAU(vor)} = I_{FAUVM}$

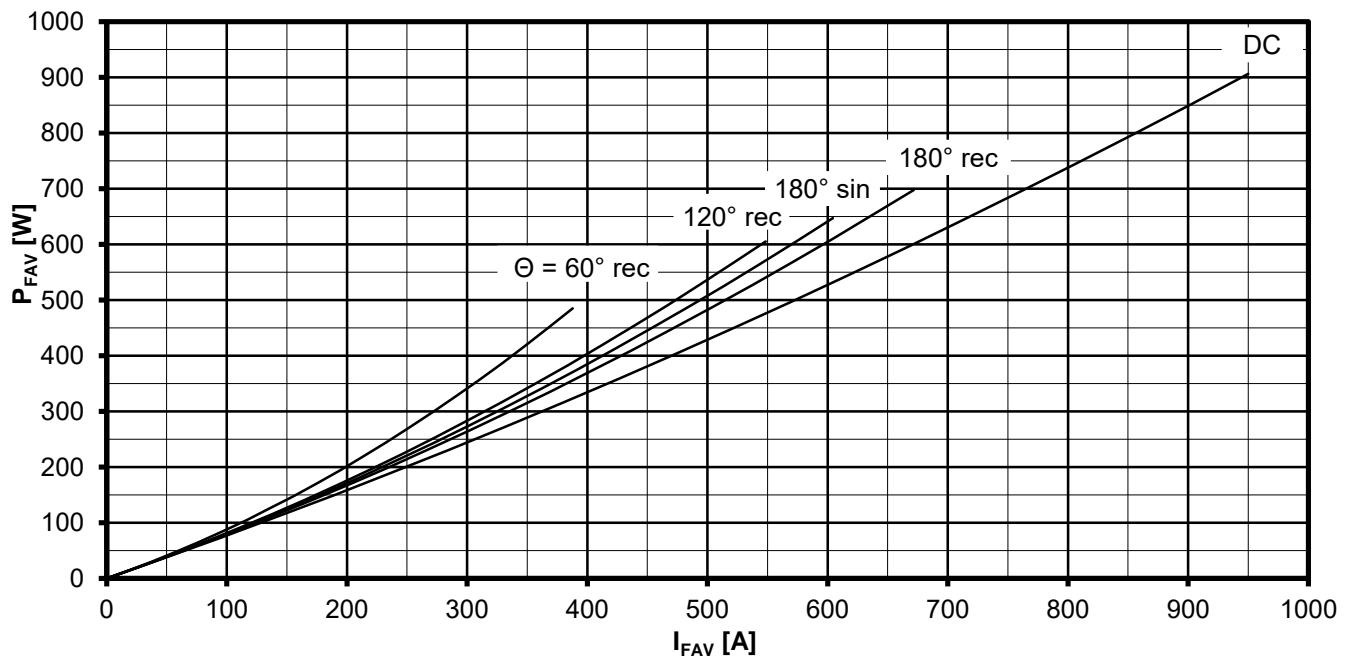
$T_a = 35^\circ\text{C}$, Forced air cooling Heatsink type: KM17 (Papst 4650)

$T_a = 45^\circ\text{C}$, Natural air cooling Heatsink type: KM17 (160W)



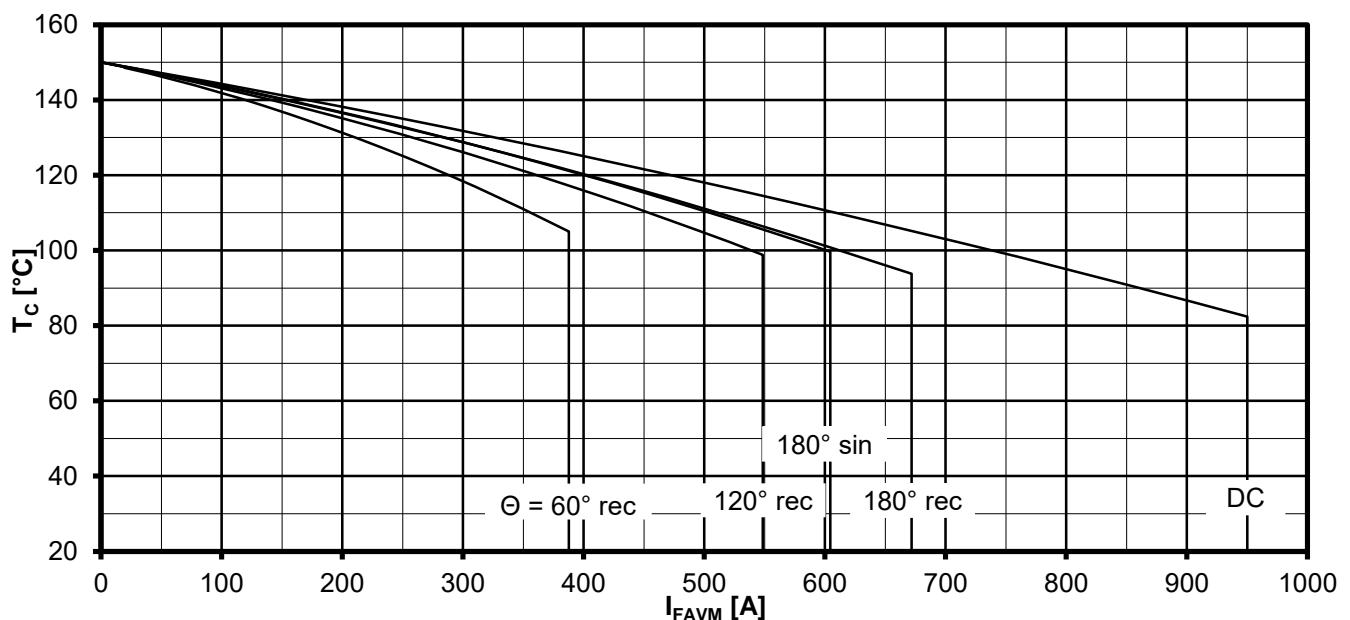
Transient thermal impedance per arm $Z_{thJC} = f(t)$

Parameter: Current conduction angle Θ



On-state power loss per arm $P_{FAV} = f(I_{FAV})$

Parameter: Current conduction angle Θ

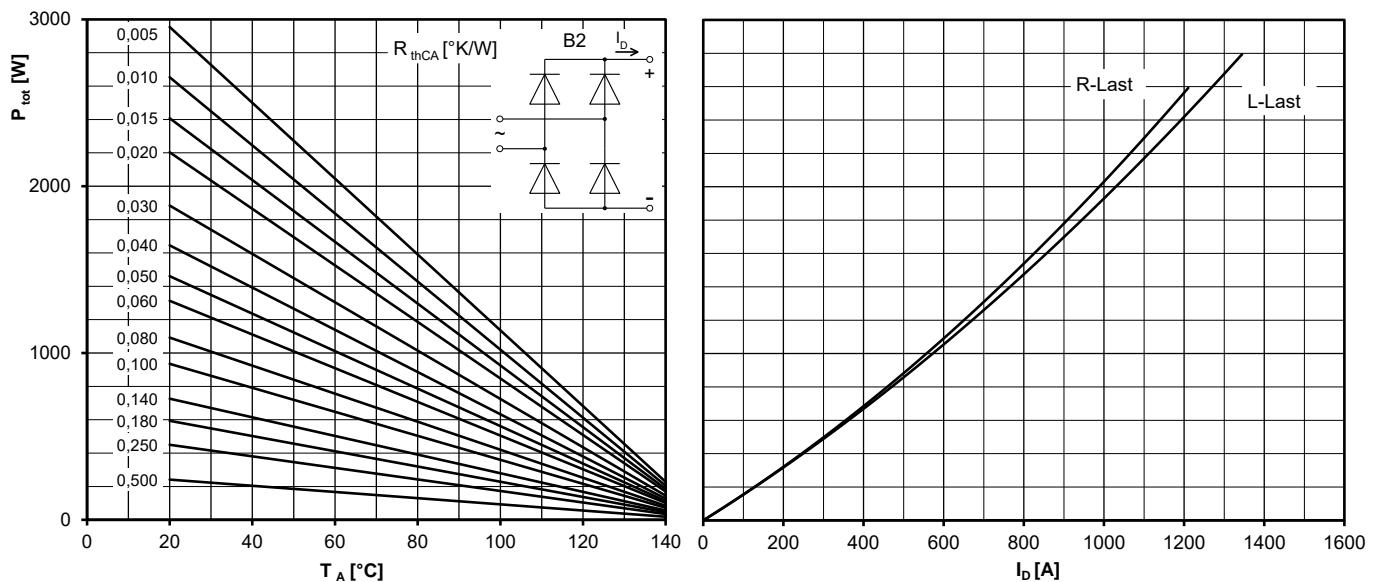


Maximum allowable case temperature $T_c = f(I_{FAV})$

Current load per arm

Calculation base P_{TAV}

Parameter: Current conduction angle Θ

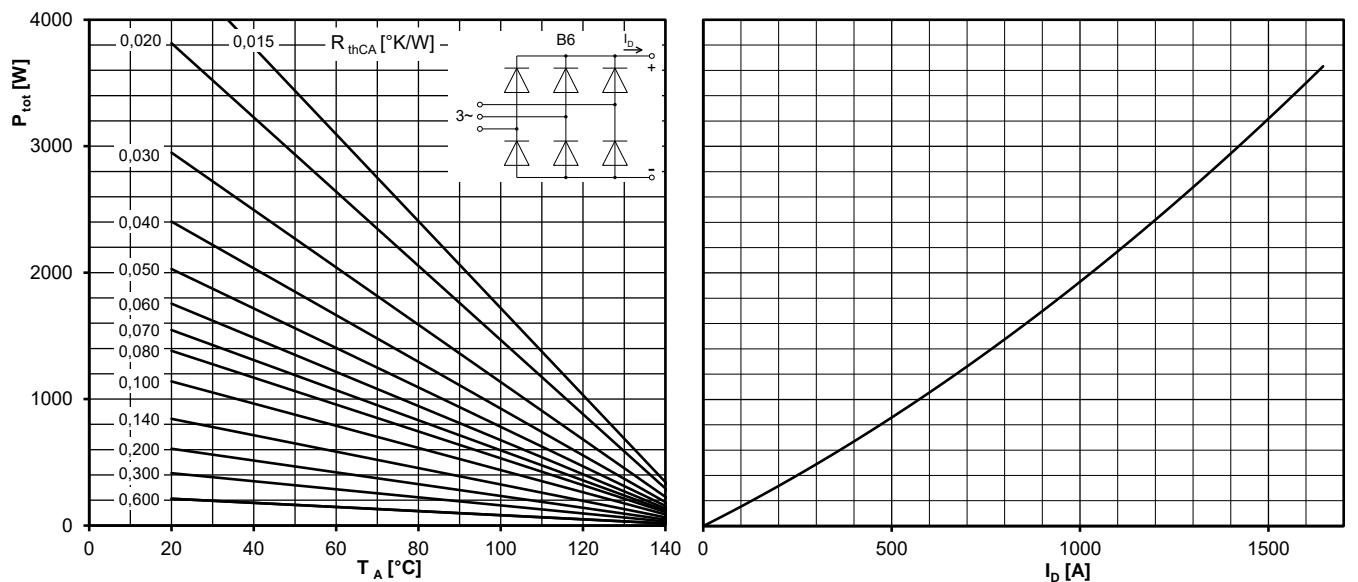


Maximum rated output current I_D

B2- Two-pulse bridge circuit

Total power dissipation at circuit P_{tot}

Parameter:
Thermal resistance cases to ambient R_{thCA}

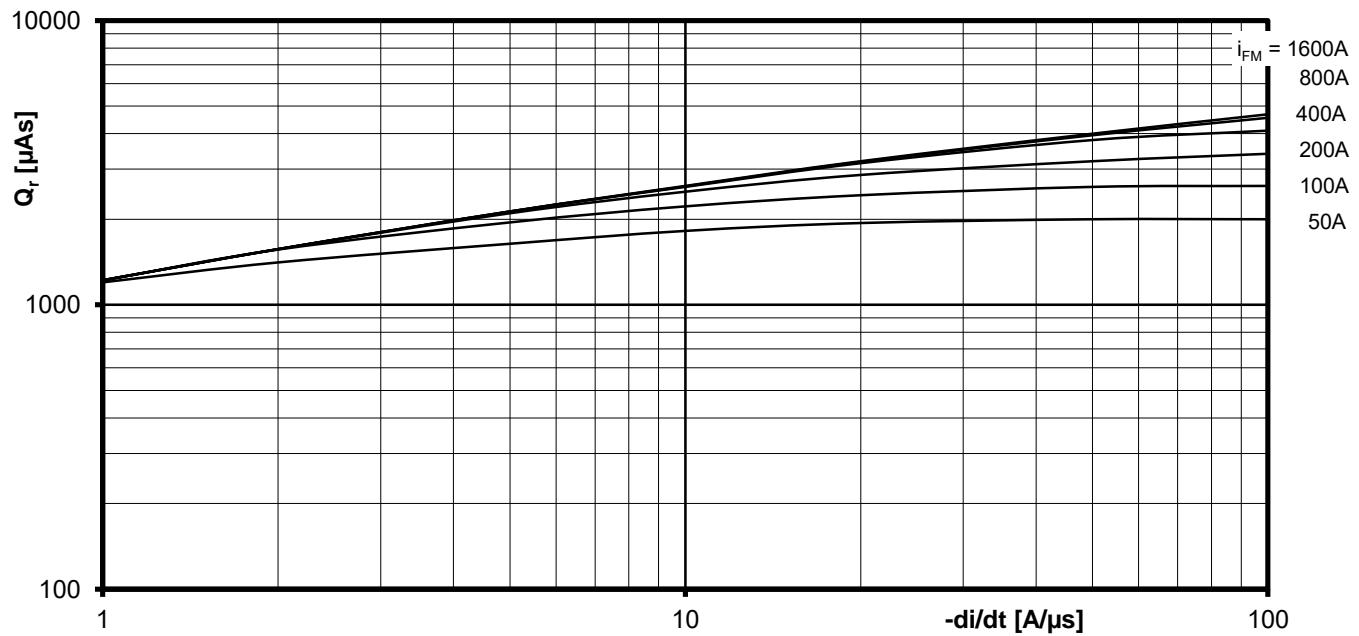


Maximum rated output current I_D

B6-Six-pulse bridge circuit

Total power dissipation at circuit P_{tot}

Parameter:
Thermal resistance cases to ambient R_{thCA}



Recovered charge $Q_r = f(-di/dt)$

$T_{vj} = T_{vjmax}$, $v_R \leq 0,5 V_{RRM}$, $v_{RM} = 0,8 V_{RRM}$

Parameter: On-state current i_{FM}