

# DRD2770F14

# **Rectifier Diode**

DS5993-1 March 2011 (LN28179)

### **FEATURES**

- Double Side Cooling
- High Surge Capability

### **KEY PARAMETERS**

$V_{RRM}$	1400V
I <sub>F(AV)</sub>	2770A
I <sub>FSM</sub>	31000A

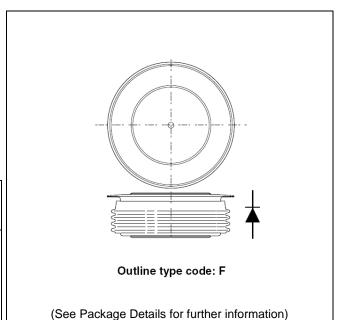


Fig. 1 Package outline

### **VOLTAGE RATINGS**

Part and Ordering Number	Repetitive Peak Voltages V <sub>RRM</sub> V	Conditions
DRD2770F14 DRD2770F12 DRD2770F10 DRD2770F08 DRD2770F06	1400 1200 1000 800 600	$V_{RSM} = V_{RRM} + 100V$

### **ORDERING INFORMATION**

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

**DRD2770F14** for a 1400V device

# **CURRENT RATINGS**

# $T_{\text{case}}$ = 75°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units		
Double Si	Double Side Cooled					
I <sub>F(AV)</sub>	Mean forward current	Half wave resistive load	3280	Α		
I <sub>F(RMS)</sub>	RMS value	-	5150	А		
I <sub>F</sub>	Continuous (direct) on-state current	-	4640	А		

# $T_{\text{case}}$ = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions		Units		
Double Si	Double Side Cooled					
I <sub>F(AV)</sub>	Mean forward current	Half wave resistive load	2770	А		
I <sub>F(RMS)</sub>	RMS value	-	4350	Α		
I <sub>F</sub>	Continuous (direct) on-state current	-	3920	Α		

# **SURGE RATINGS**

Symbol	Parameter	Parameter Test Conditions		Units
I <sub>FSM</sub>	Surge (non-repetitive) on-state current	10ms half sine, T <sub>case</sub> = 190°C	31.0	kA
l <sup>2</sup> t	I <sup>2</sup> t for fusing	$V_R = 0$	4.81	MA <sup>2</sup> s

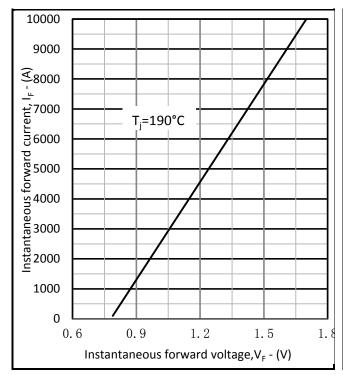
# THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance – junction to case	Double side cooled	DC	-	0.02	°C/W
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink	Double side cooled	DC	-	0.005	°C/W
T <sub>vj</sub>	Virtual junction temperature	Blocking V <sub>DRM</sub> / <sub>VRRM</sub>		-40	190	°C
T <sub>stg</sub>	Storage temperature range			-40	190	°C
F <sub>m</sub>	Clamping force			18	26	kN

# **CHARACTERISTICS**

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V <sub>FM</sub>	Forward voltage	At 1500A peak, T <sub>case</sub> = 25°C	-	1.05	V
I <sub>RM</sub>	Peak reverse current	At V <sub>DRM</sub> , T <sub>case</sub> = 190°C	-	150	mA
Os	Q <sub>S</sub> Total stored charge	I <sub>F</sub> = 2000A, dI <sub>RR</sub> /dt =10A/μs	-	4000	μC
345		$T_{case} = 190^{\circ}C, V_{R} = 100V$			
$V_{TO}$	Threshold voltage	At T <sub>vj</sub> = 190°C	-	0.78	V
r <sub>T</sub>	Slope resistance	At T <sub>vj</sub> = 190°C	-	0.092	mΩ

# **CURVES**



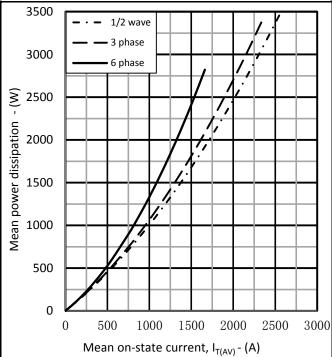
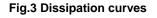


Fig.2 Maximum forward characteristics



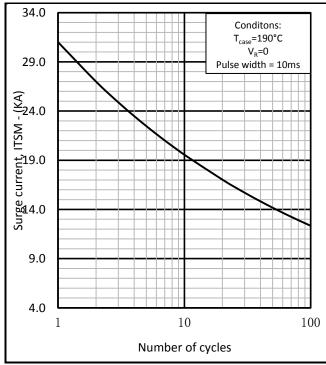


Fig.4 Surge (Non-Repetitive) Forward current vs time

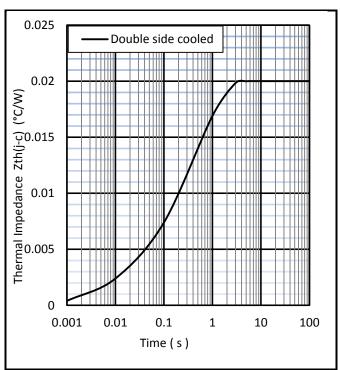
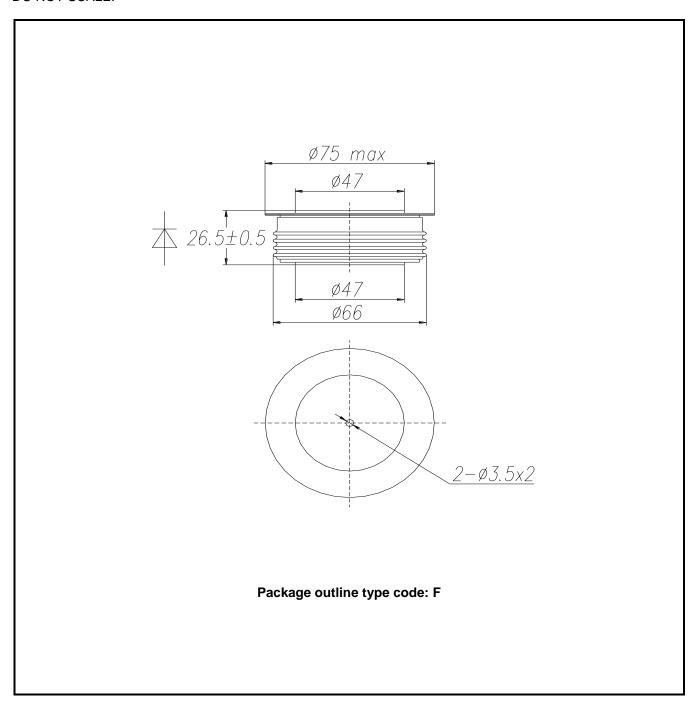


Fig.5 Maximum (limit) transient thermal impedancejunction to case

# **PACKAGE DETAILS**

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



#### Note:

Some packages may be supplied with gate and or tags.

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No Annotation:

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Target Information: This is the most tentative form of information and represents a very preliminary specification.

No actual design work on the product has been started.

**Preliminary Information:**The product design is complete and final characterisation for volume production is in progress. The datasheet represents the product as it is now understood but details may change.

The product has been approved for production and unless otherwise notified by Dynex any product ordered will be supplied to the **current version of the data sheet prevailing at the** 

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